Micro-Power Voltage Detectors

General Description

The ET9818A25SL is a micro-power voltage detector supervising the power supply voltage level for microprocessors (μP) or digital systems. It provides internally fixed threshold levels of 2.5V. It features low supply current of 3μA.

The ET9818A25SL performs supervisory function by sending out a reset signal whenever the V_{DD} voltage falls below a preset threshold level. This reset signal will last the whole period before V_{DD} recovering. Once V_{DD} recovered up crossing the threshold level, the reset signal will be released after a certain delay time.

Features

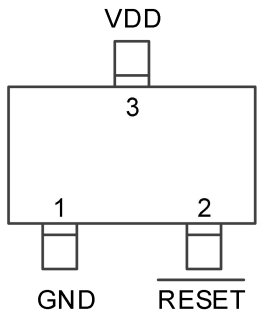
- Internally Fixed Threshold 2.5V
- High Accuracy ±1.5%
- Low Supply Current 3μA
- No External Components Required
- Quick Reset within 20μs
- Built-in Recovery Delay 10μs
- Low Functional Supply Voltage 0.9V
- N-Channel Open-Drain Output
- Package information:

Part No.	Package	Packing Option	MSL
ET9818A25SL	SSOT23-3	Tape and Reel, 3K/Reel	Level 3

Application

- Computers
- Controllers
- Intelligent Instruments
- Critical μP and μC Power Monitoring
- Portable/Battery-Powered Equipment

Pin Configuration

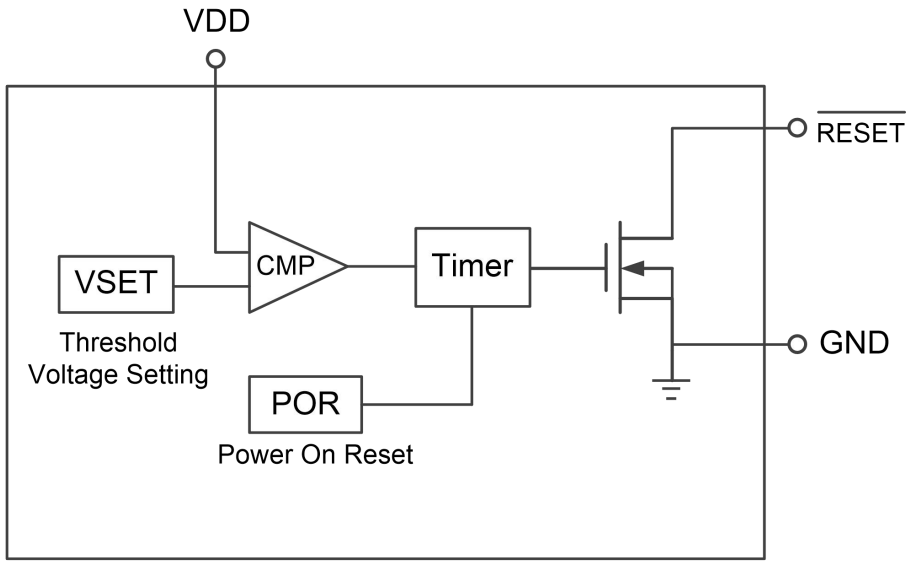


SSOT23-3
TOP VIEW

Pin Function

Pin NO.	Pin Name	Description
1	GND	Ground
2	$\overline{\text{RESET}}$	Active Low Open-Drain Reset Output
3	VDD	Power Pin

Block Diagram



Absolute Maximum Ratings(1)

Characteristic	Symbol	Min	Max	Unit
Supply Voltage	V_{DD}	-0.3	6.5	V
Input Current	I_{VDD}		20	mA
Junction Temperature	T_J	-40	+150	°C
Package Thermal Resistance (3)	θ_{JA}		250	°C/W
Storage Temperature Range	T_{STG}	-65	+150	°C
Lead Temperature (Soldering, 10sec.)	T_{LEAD}		260	°C
Power Dissipation	P_D		400	mW
Human Body Model, JESD22-A114	ESD(2)	±4000		V
Charged Device Model,JESD22-C101		±500		V

Note1. Stresses listed as the above "Absolute Maximum Ratings" may cause permanent damage to the device.These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.

Note2. Devices are ESD sensitive. Handling precaution recommended.

Note3. θ_{JA} is measured in the natural convection at T_A = 25°C on a low effective thermal conductivity test board of JEDEC 51-3 thermal measurement standard.

Recommended Operating Conditions (4)

Characteristic	Symbol	Min	Max	Unit
Supply Voltage	V_{DD}	0.9	6.0	V
Operating Ambient Temperature	T_A	-40	85	°C
Power Dissipation	P_D		400	mW

Note4.The device is not guaranteed to function outside its operating conditions.

Electrical Characteristics

(V_{DD} = 3V, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Operating VDD (VOUT) Range	V_{DD}		0.9		6	V
Supply Current	I_{DD}	V_{DD} = 4.5V T_A = 25°C		3	8	μA
Reset Threshold	V_{TH_L}	T_A = 25°C	2.463	2.5	2.537	V
	V_{TH_H}		$V_{TH_L}+V_{HYS}$			V
Threshold Voltage Accuracy	ΔV_{TH}	T_A = 25°C	-1.5		+1.5	%
Hysteresis Width	V_{HYS}			0.015 V_{TH_L}	0.025 V_{TH_L}	V
V_{DD} Drop to Reset Delay[5]	t_{RD}	Drop = V_{TH} -125mV		20		μs
Reset Active Time Out Period	t_{RP}	$V_{DD} \geq 1.03 \times V_{TH}$		10		μs
RESET Output Voltage Low[6]	V_{OL1}	0 < V_{DD} < $V_{TH,}$ R-pull up=100k			0.2	V
	V_{OL2}	V_{DD} = V_{TH} -0.1,I_{SINK} =3.5mA			0.2	

Note5. Guaranteed by design and characterization, not a FT item.

Note6. The voltage V_{OL} can be calculated by V_{OL} = VDD − Ir * R. Where R is the pull-up resistor and Ir is the current flowing through the pull-up resistor. For typical application (R=100kΩ), V_{OL} is less than 0.2V.

Application Information

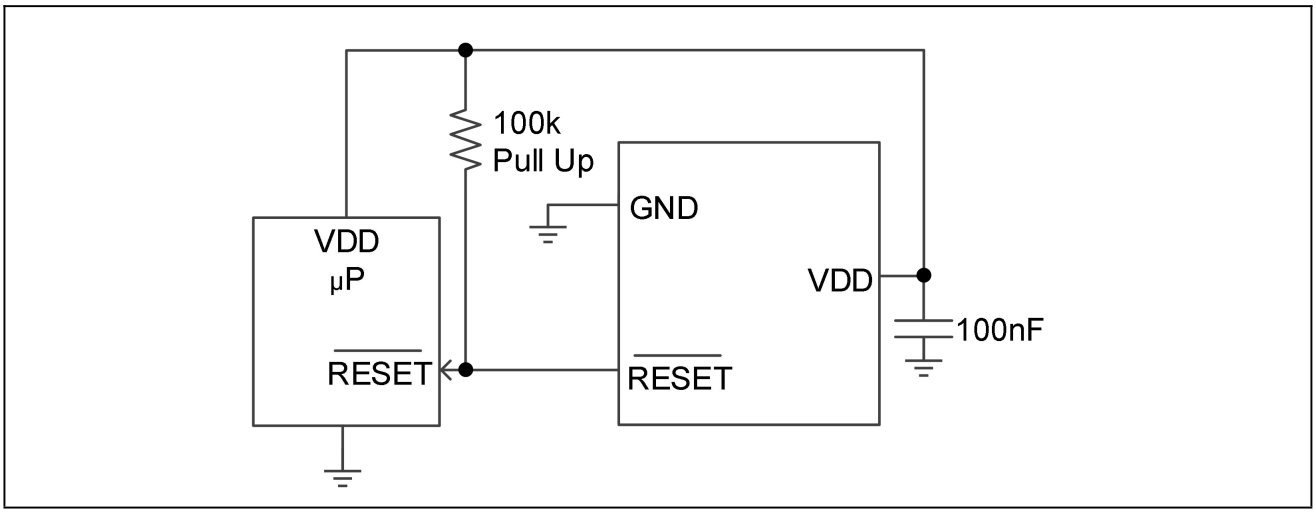
Multiple Supplies

Mainly, the pull-up connected to the ET9818A25SL will connect to the supply voltage that is being monitored at the IC' s VDD pin. However, some systems may use the open-drain output to level-shift from the monitored supply to reset circuitry powered by some other supply.

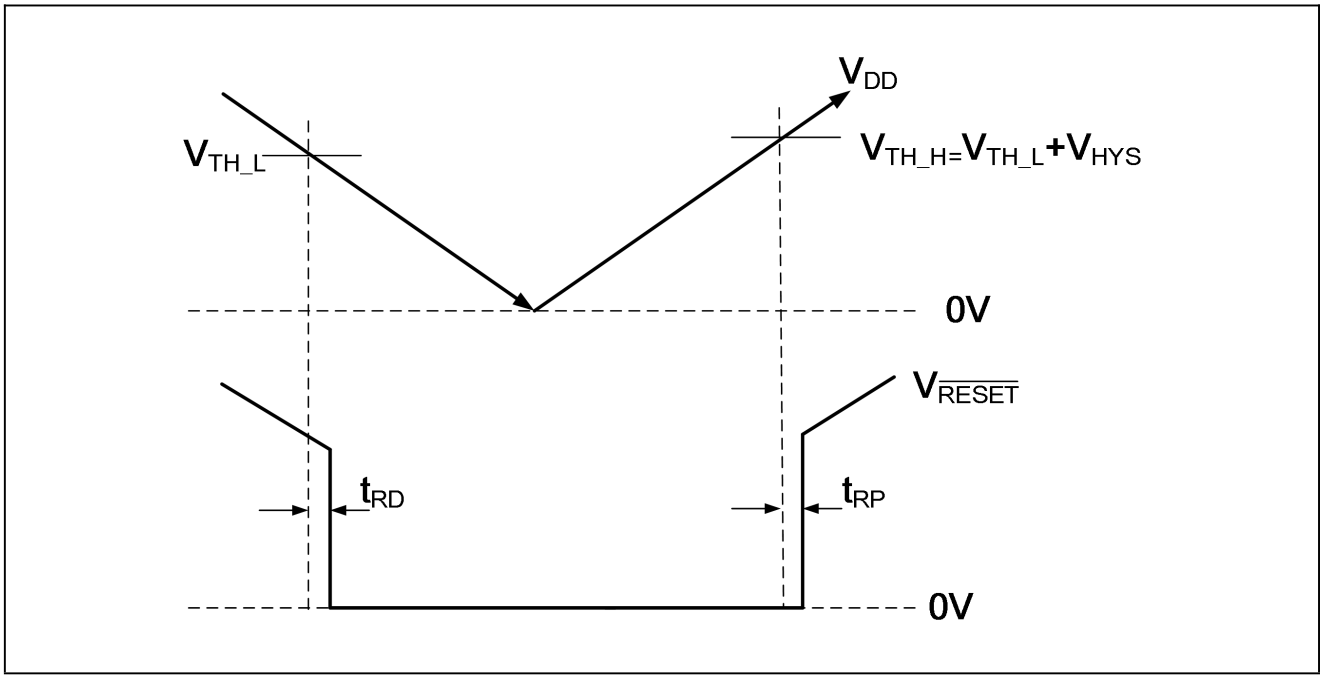
Benefits of Highly Accurate Reset Threshold

Most μP supervisor ICs have reset threshold voltages between 1% and 1.5% below the value of nominal supply voltages. This ensures a reset will not occur within 1% of the nominal supply, but will occur when the supply is 1.5% below nominal.

Test Circuit

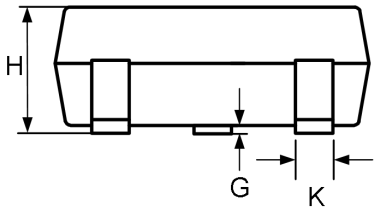


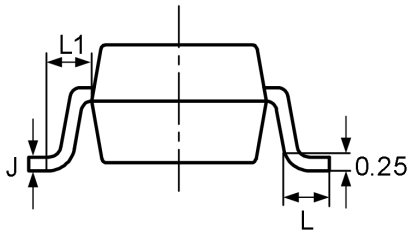
Functional Diagram

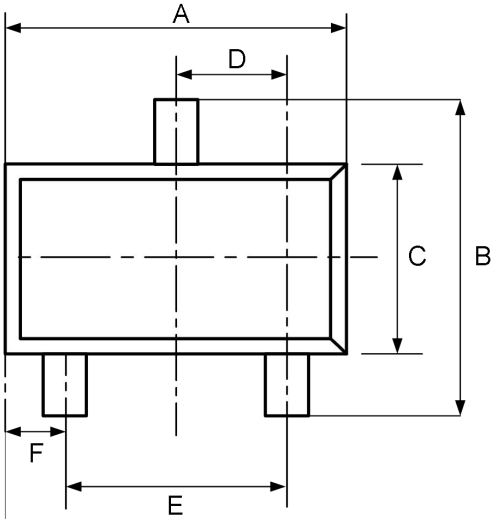


Package Dimension

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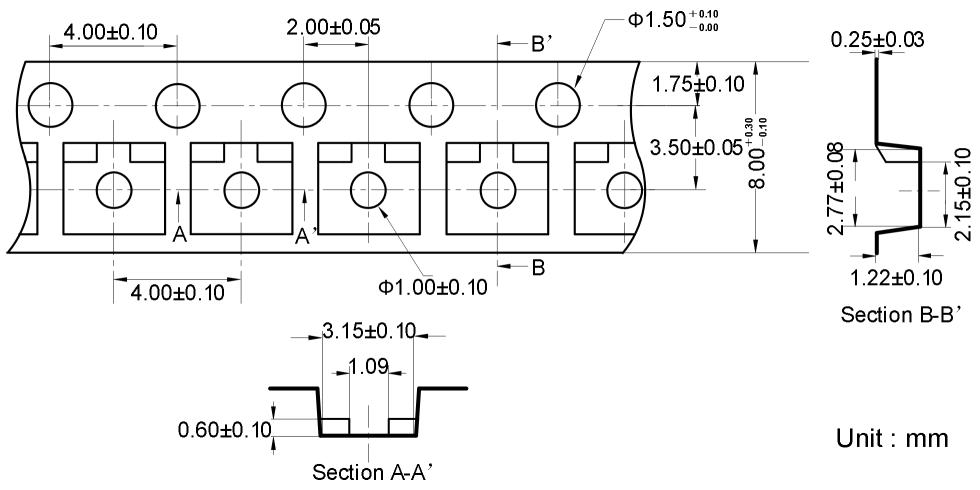
COMMON DIMENSIONS

(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	MAX
A	2.800	3.040
B	2.100	2.640
C	1.200	1.400
D	0.890	1.030
E	1.780	2.050
F	0.450	0.600
G	0.013	0.100
H	0.900	1.110
J	0.085	0.180
K	0.370	0.510
L	0.300	0.500
L1	0.500	0.600

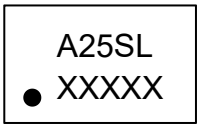
Tape Information

SSOT23-3



Marking Information

ET9818A25SL



A25SL - Part Number

XXXXX - Tracking Number

Note: X (Tracking Number) is variable, according to the wafer lot number.

Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2026-08-07	Original version	Huyt	Chenh	Liujy
1.1	2026-08-12	Add V_{TH_H}	Huyt	Chenh	Liujy