1A Low Dropout LDO

General Description

The ET5A0XXY1B is CMOS-based low-dropout, low-power linear regulators, offering 1A with low dropout voltage, high ripple rejection, high output accuracy and low supply current. The ET5A0XXY1B consist of an accurate voltage-reference block, an error amplifier, a voltage-setting resistor net, a PMOSFET pass device, a thermal-shutdown circuit, and a current limit circuit with short protection.

The ET5A0XXY1B use a type of outstanding CMOS process to minimize the supply current. A low on-resistance PMOS pass device is equipped for lower dropout voltage. ET5A0XXY1B also possess the CE function to save more energy and extend the battery life.

The ET5A0XXY1B output current up to 1.0A. The output voltage fixed to 1.2V,1.8V,2.5V,2.8V,3.0V,3.3V etc.

The ET5A0XXY1B is available in the DFN6 package (2mm×2mm).

Features

- Wide Input Voltage Range: 1.8V to 5.5V
- Output Current: 1.0A optional
- Fixed Output Voltage : 1.2V,1.8V,2.5V,2.8V,3.0V,3.3V etc
- Very Low IQ: 110μA
- Excellent Load/Line Transient Response
- Line Regulation: 0.02% typical
- Built-in over-current Protection and Thermal Shutdown Circuit
- Built-in Auto-discharging Circuit
- Reverse Current Protection
- Package: DFN6 (2mm×2mm)

Applications

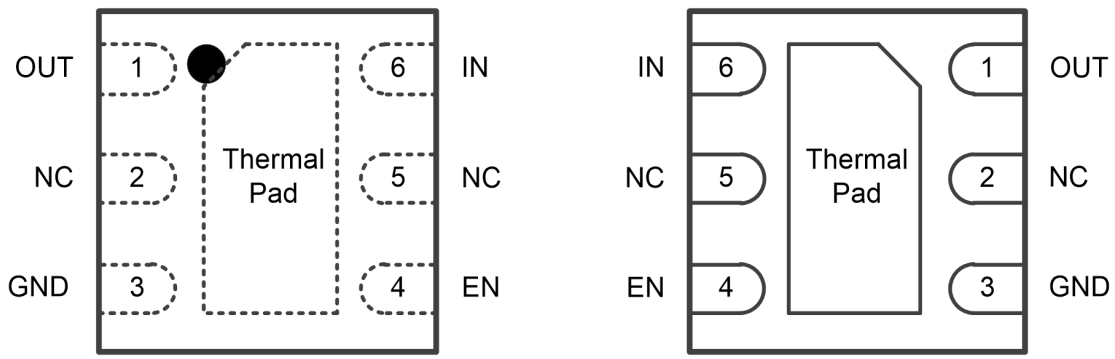
- Constant-voltage power supply for battery-powered device
- Constant-voltage power supply for TV, notebook PC and home electric appliance
- Constant-voltage power supply for portable equipment
- Label Information

Device Information

ET 5A0 XX Y1 B

Output Voltage		Package		Auto-discharging Function	
XX	Output is X.XV	**Y1**	DFN6 (2×2)	**B**	Available

Pin Configuration

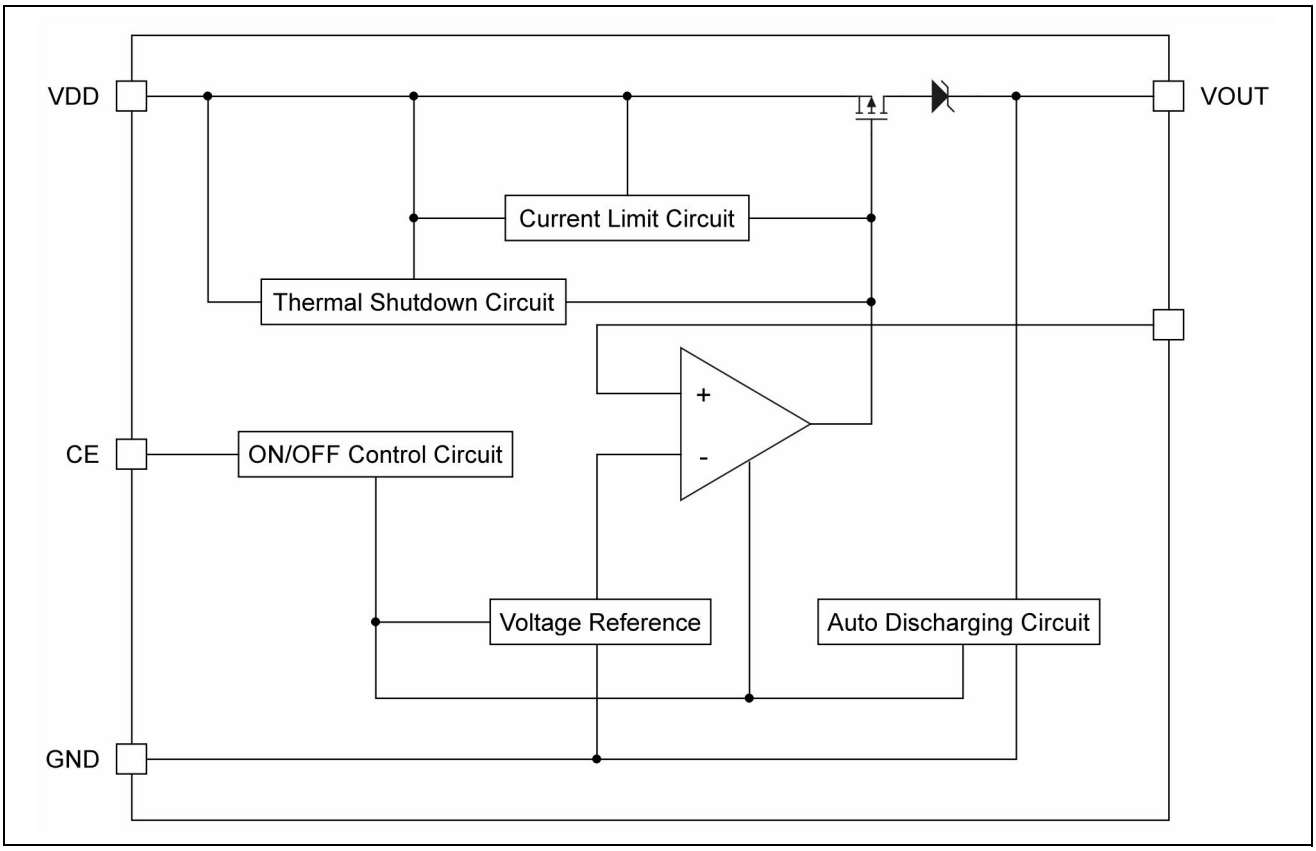


Top View Bottom View

Pin Function

Pin	Symbol	Pin Description
1	VOUT	Output Pin
2	NC	No connect
3	GND	Ground Pin
4	CE	Chip Enable Pin
5	NC	No connect
6	VDD	Input Pin
	Thermal pad	Connect to GND or leave floating. Do not connect to any potential other than GND.

Block Diagram



Functional Description

Input Capacitor

A 1μF ceramic capacitor is recommended to connect between VDO and GND pins to decouple input power supply glitch and noise. The amount of the capacitance may be increased without limit. This input capacitor must be located as close as possible to the device to assure input stability and less noise. For PCB layout, a wide copper trace is required for both VIN and GND.

Please pay attention , in the case of high impedance of the power supply, the input capacitance of the IC is small or the capacitor is not connected, the oscillation may occur. When the capacitance value of the output capacitor is greater than the capacitance value of the input capacitor, it is possible to generate oscillation.

Output Capacitor

An output capacitor is required for the stability of the LDO. The recommended output capacitance is 1μF, ceramic capacitor is recommended, and temperature characteristics are X7R or X5R. Higher capacitance values help to improve load/line transient response. The output capacitance may be increased to keep low undershoot/overshoot. Place output capacitor as close as possible to VOUT and GND pins.

CE Pin Operation

The ET5A0XXY1B is turned on by setting the CE pin to “H”. When the CE pin is used, connect the CE pin with VDD to keep the LDO in operating mode.

Current Limit Protection

When output current of VOUT pin is higher than current limit threshold or the VOUT pin is direct short to GND, the current limit protection will be triggered and clamp the output current at a predesigned level to prevent over-current and thermal damage. ET5A0XXY1B output current limit will be 1.4A.

Thermal Shutdown Protection

Thermal protection disables the output when the junction temperature rises to approximately +155°C, allowing the device to cool down. When the junction temperature reduces to approximately +120°C the output circuit is enabled again. Depending on power dissipation, thermal resistance, and ambient temperature, the thermal protection circuit may cycle on and off. This cycling limits the heat dissipation of the regulator, protecting it from damage due to overheating.

Reverse Current Protection Circuit

Under normal conditions,when VOUT is higher than VIN, the parasitic diode of Pch output transistor becomes forward direction. As a result, the current flows from VOUT pin to VDD pin.

The ET5A0XXY1B switches the mode to the reverse current protection mode before VIN becomes lower than VOUT by connecting the parasitic diode of Pch output transistor to the backward direction, and connecting the gate to VOUT pin. As a result, the Pch output transistor is turned off. However, from VOUT pin to GND pin, via the internal divider resistors, very small current IREV flows.

Auto Discharging

When the CE pin set to “L”, the output circuit will be disable immediately, and the Auto-Discharging circuit will be turned on to discharge the electric charge on output capacitor, and decrease the voltage of VOUT in very short time. The Auto-Discharging function is optional.

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Symbol	Parameter	Rating	Unit
V_{IN}	Input Voltage	-0.3 to 6	V
V_{CE}	Input Voltage (CE Pin)	-0.3 to 6	V
V_{OUT}	Output Voltage	-0.3 to V_{IN}+0.3	V
I_{MAX}	Maximum Load Current	1000	mA
P_D	Maximum Power Consumption	1200	mW
$R_{\theta JA}$	Junction-to-ambient thermal resistance	105	°C/W
T_J	Operating Junction Temperature	-40 to 150	°C
T_{STG}	Storage Temperature	-65 to 150	°C
T_{SOLD}	Lead Temperature (Soldering, 5sec)	260	°C
V_{ESD}	Human Body Model (JEDEC JS-001)	±4000	V
	Charged Device Model (JESD22-C101)	±1500	V

Recommended Operating Conditions

Symbol	Item	Rating	Unit
V_{IN}	Input Voltage	1.8 to 5.5	V
I_{OUT}	Output Current	0 to 1000	mA
T_A	Operating Ambient Temperature	-40 to 85	°C
C_{IN}	Effective Input Ceramic Capacitor Value	1 to 10	μF
C_{OUT}	Effective Output Ceramic Capacitor Value	1 to 10	μF
ESR	Input and Output Capacitor Equivalent Series Resistance	5 to 100	mΩ

Electrical Characteristics

(Unless otherwise noted ,V_{IN}= V_{SET}+1.0V, Ta = -40°C ~85°C , C_{IN}=C_{OUT}=1uF(effective capacitance [4]); Typical values are at V_{IN}= V_{SET}+1.0V,T_A = 25°C)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input Voltage Operation Range	V_{IN} [1]	V_{IN} ≥ V_{SET}+0.5V	1.8		5.5	V
Output Voltage	V_{OUT}	I_{OUT}=1mA,T_A =25°C	×0.98		×1.02	V
		I_{OUT}=1mA~1A,T_A =25°C	-30		+30	mV
Dropout Voltage	V_{DIF}[2]	1.2V ≤ V_{OUT} < 2.6V, I_{OUT}=1A, V_{OUT} dropping to 0.98×V_{OUT}		170	220	mV
		2.6V ≤ V_{OUT} < 3.3V, I_{OUT}=1A, V_{OUT} dropping to 0.98×V_{OUT}		150	185	mV
		3.3V ≤ V_{OUT} ≤ 4.3V,I_{OUT}=1A, V_{OUT} dropping to 0.98×V_{OUT}		135	170	mV
Supply Quiescent Current	I_{Q_ON}	Active mode: V_{CE}=high. I_{OUT}=0mA	70	110	150	μA
Supply Shutdown Current	$I_{Q\ OFF}$	V_{CE}=0V		1	3	μA
Output Voltage Line Regulation	Reg_{LINE}	V_{SET}+0.5V≤V_{IN}≤5.5V (V_{IN}≥1.8V),I_{OUT} = 10mA ($\Delta V_{OUT}/\Delta V_{IN}/V_{OUT}$)		0.02	0.1	%/V
Output Voltage Load Regulation	Reg_{LOAD}	I_{OUT} from 1mA to 1A V_{IN}=Vset+0.5V (ΔV_{OUT})		5	30	mV
Line Transient (The absolute value of the output change)	V_{TRLN} [3]	V_{OUT}=3.3V ,I_{OUT} = 1mA, V_{IN} =4.3V to 5.5V in 10us, T_A =25°C		15	30	mv
		V_{OUT}=3.3V ,I_{OUT} = 1mA, V_{IN} =5.5V to 4.3V in 10us, T_A =25°C		15	30	
Load Transient (The absolute value of the output change)	V_{TRLD} [3]	V_{OUT}=3.3V ,V_{IN} =4.3V, I_{OUT} from 1mA to 1000mA in 10us,T_A =25°C		75	120	mv
		V_{OUT}=3.3V,V_{IN}=4.3V, I_{OUT} from 1000mA to 1mA in 10us,T_A =25°C		75	120	
Output Current	I_{OUT}		1000			mA
Over Current Limit	I_{LMT}	V_{OUT}=3.3V,V_{IN}=4.3V, T_A =25°C	1050	1400	1900	mA
Short Current Limit	I_{SHORT}	V_{OUT}=0V, Ta=25°C	60	130	200	mA
Power Supply Rejection Ratio	PSRR [3]	f=1kHz, C_{OUT}=1μF, I_{OUT}=20mA, V_{IN}=4.3V, T_A =25°C		70		dB

Electrical Characteristics (Continued)

(Unless otherwise noted ,V_{IN}= V_{SET}+1.0V, Ta = -40°C ~85°C , C_{IN}=C_{OUT}=1uF(effective capacitance [(4)]); Typical values are at V_{IN}= V_{SET}+1.0V,Ta = 25°C)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Noise	e_N [(3)]	10Hz to 100kHz, I_{OUT} = 30mA, C_{OUT} = 1μF, T_A =25°C		50* V_{OUT}	80* V_{OUT}	μV_{RMS}
CE Low Threshold	V_{IL}	V_{IN}=1.8 to 5.5V			0.3	V
CE High Threshold	V_{IH}	V_{IN}=1.8 to 5.5V	1			V
CE Pull-down Current	I_{CE}	V_{IN}=4.3V, V_{CE}=4.3V, T_A =25°C		0.3	1	μA
Output resistance of auto discharge at off state	R_{LOW}	V_{EN}=0V,V_{IN}=4V,I_{OUT}=10mA	30	60	100	Ω
Thermal Shutdown Temperature	T_{TSD} [(3)]	Junction Temperature Rising	135	155	175	°C
Thermal Shutdown Released hysteresis	T_{HYS} [(3)]	Junction Temperature falling from shutdown	20	40	60	°C

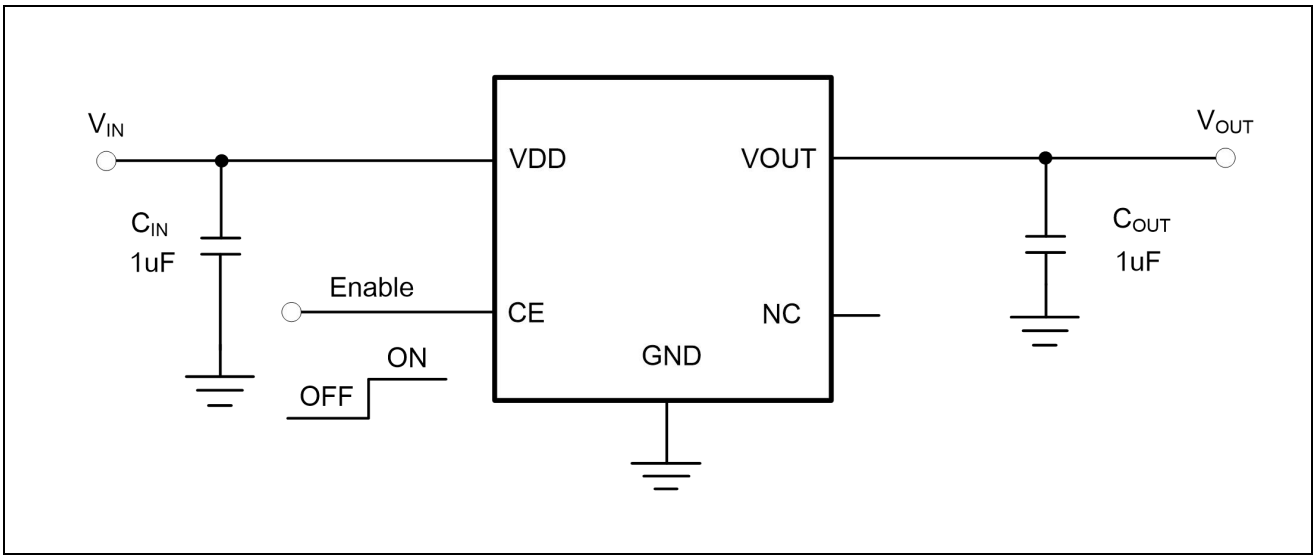
Note (1): V_{IN} means internal circuit can work normal. If V_{IN} <V_{OUT}, Reverse current protection circuit will work .

Note (2): V_{DROP} FT test method: test the V_{OUT} voltage at V_{SET}+$V_{DROPMAX}$ with output current.

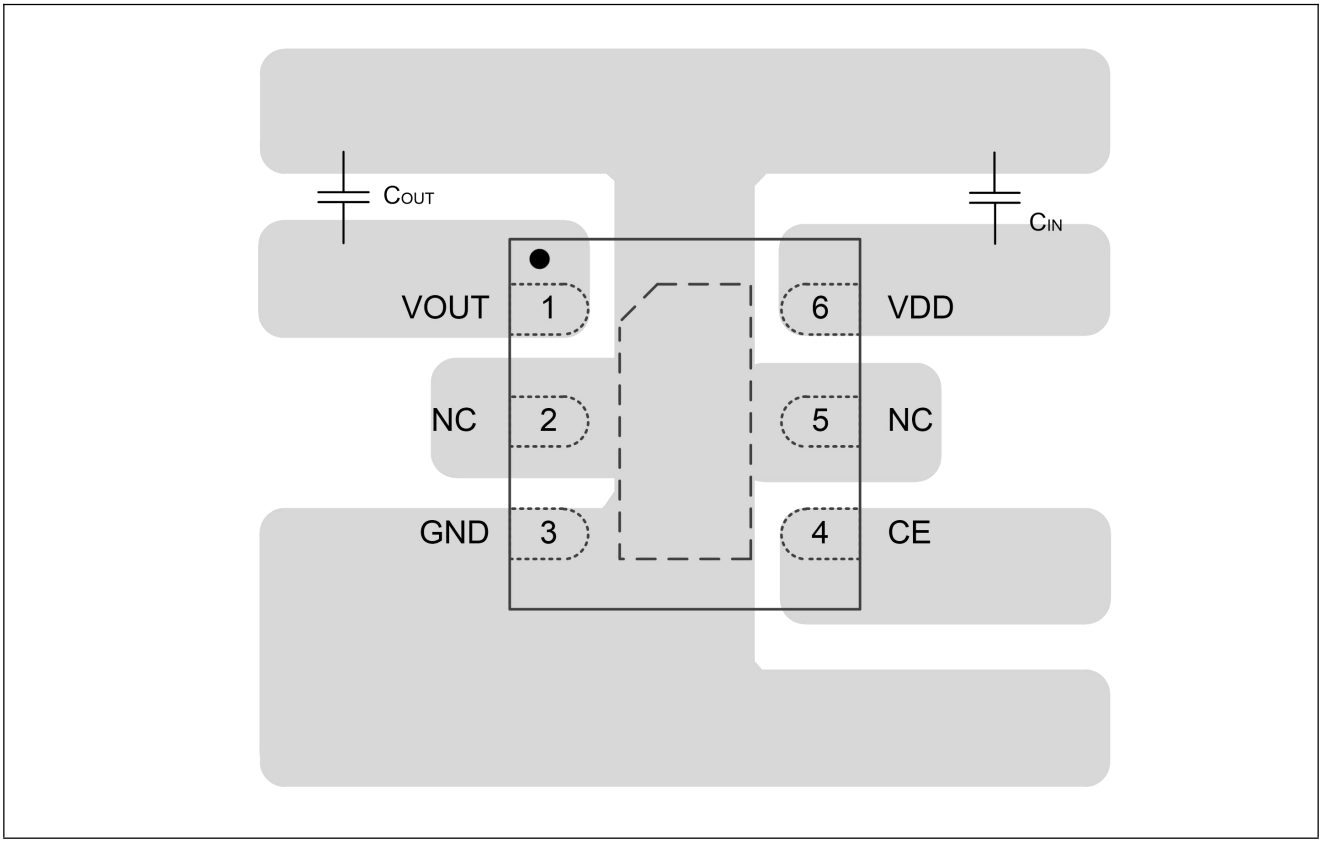
Note (3): Guaranteed by design and characterization. not a FT item.

Note (4): Effective capacitance, including the effect of DC bias, tolerance and temperature.

Application Circuits

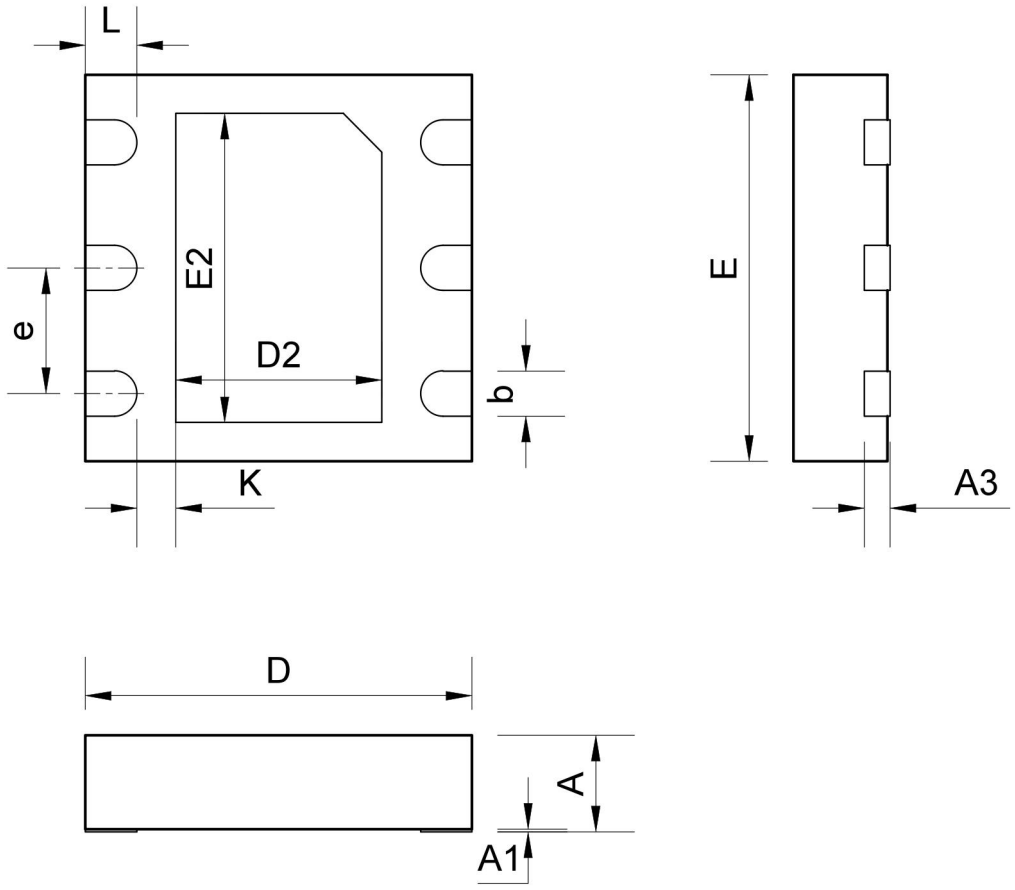


PCB Layout Guide



Package Dimension

DFN6

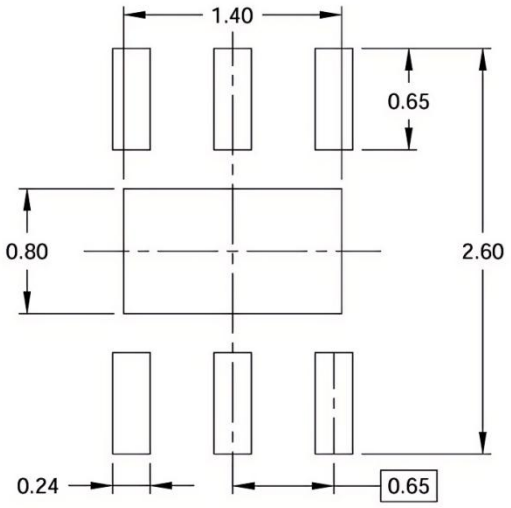


COMMON DIMENSIONS

(UNITS OF MEASURE=MILLIMETER)

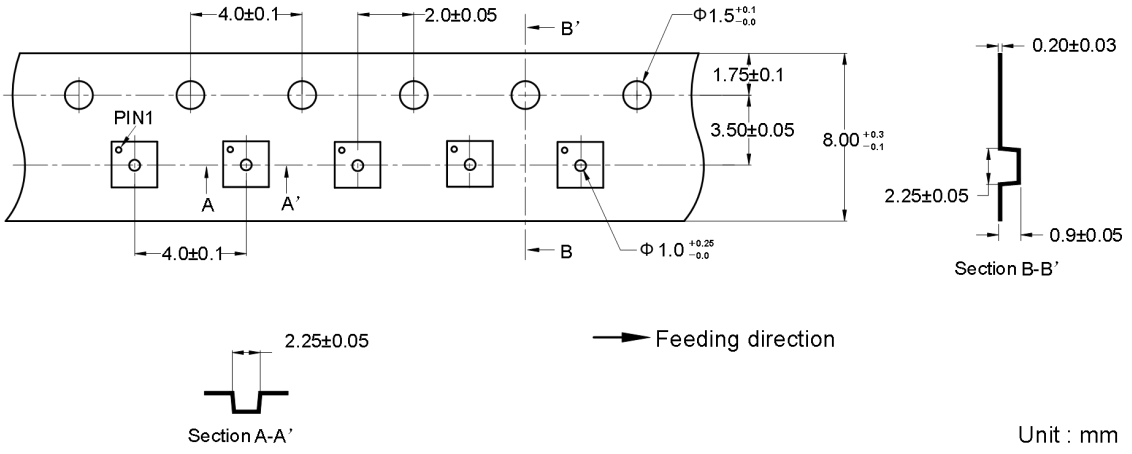
SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A3		0.20REF	
b	0.25	0.35	0.45
D	1.90	2.00	2.10
E	1.90	2.00	2.10
D2	0.65	0.80	0.90
E2	1.35	1.50	1.60
e		0.65BSC	
L	0.30	0.35	0.40
k	0.20	--	--

Recommended PCB Land Pattern

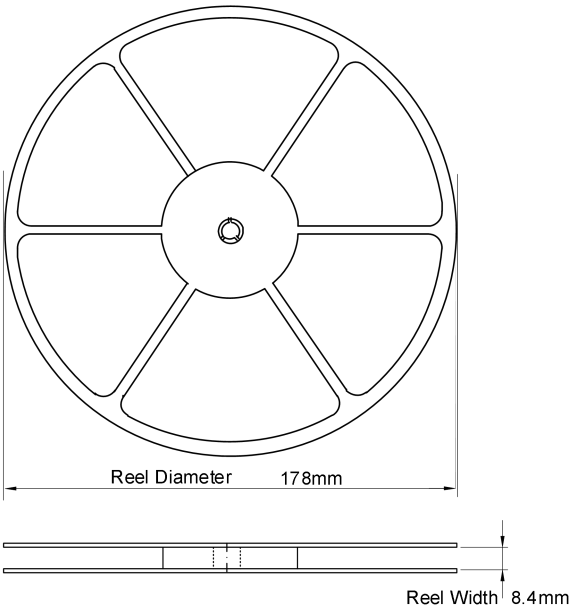


Recommended Land Pattern Unit:mm

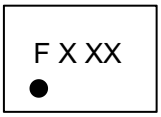
Tape Information



Reel Information



Marking



F = Part Number (ET5A0XXY1B)

X = V_{OUT} Version (C=1.8V,F=2.5V,E=3.3V)

XX = Tracking Number

Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2020-12-28	Initial version	Liuyg	Liuyg	Liujy
1.1	2023-3-15	Update to fixed Version	Shib ,Liwc	Liuyg	Liujy
1.2	2025-7-1	Add Reel tape information	Shib	Liuyg	Liujy