Single Bus Buffer Gate with 3-State Output

General Description

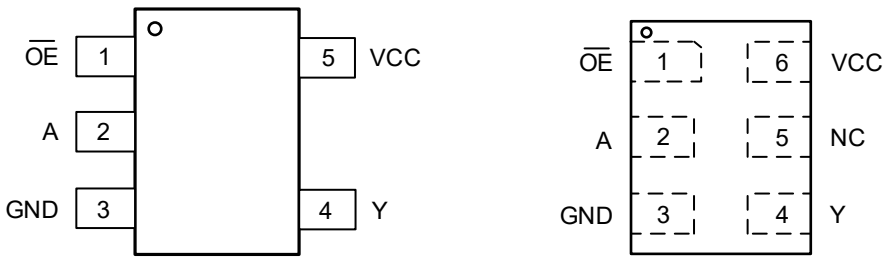
The ET74AHCT1G125 device is a single bus buffer gate/line driver with 3-state output. The output is disabled when the output-enable ($\overline{OE}$) input is high. When $\overline{OE}$ is low, data is passed from the A input to the Y output.

Features

- Designed for 4.5V to 5.5V V_{CC} Operation
- Over-voltage Tolerant Inputs Accept Voltages to 5.5V
- ±8mA Balanced Output Sink and Source Capability
- Near Zero Static Supply Current Substantially Reduces System Power Requirements
- ESD Protection Complies with JESD22 Standard
  -- HBM: ±4000V Pass (JEDEC JS-001)
  -- CDM: ±1000V Pass (JEDEC JS-002)
- Latch-up Performance Exceeds ±100mA per JEDEC JESD78F
- Part No. and Package Information

Part No.	Package	Packing Option	MSL
ET74AHCT1G125	SC70-5 (1.3mm × 2.1mm)	Tape and Reel, 3K/Reel	1
ET74AHCT1G125T	SOT23-5 (1.6mm × 2.9mm)	Tape and Reel, 3K/Reel	3
ET74AHCT1G125Y	DFN6 (1.0mm × 1.5mm)	Tape and Reel, 3K/Reel	1

Pin Configuration



SC70-5/SOT23-5 DFN6

Figure1. Pin Configuration

Pin Function

SC70-5/ SOT23-5

Pin No.	Pin Name	Function
1	$\overline{OE}$	Enable Input
2	A	Input
3	GND	Ground
4	Y	Output
5	VCC	Supply Voltage

DFN6

Pin No.	Pin Name	Function
1	$\overline{OE}$	Enable Input
2	A	Input
3	GND	Ground
4	Y	Output
5	NC	No Connect
6	VCC	Supply Voltage

Block Diagram

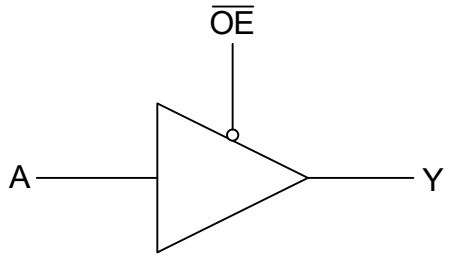


Figure2. Logic Symbol

Function Table

Input		Output
$\overline{OE}$	**A**	**Y**
L	L	L
L	H	H
H	X	Z

Absolute Maximum Ratings

Symbol	Parameter		Value	Unit
V_{CC}	DC Supply Voltage (VCC Pin)		-0.5 to 7.0	V
V_I	DC Input Voltage (1)		-0.5 ≤ V_I ≤ 7.0	V
V_O	DC Output Voltage Output in Higher or Low State		-0.5 to V_{CC} + 0.5	V
I_{IK}	DC Input Diode Current, V_I < GND		-20	mA
I_{OK}	DC Output Diode Current, V_O < GND, V_O > V_{CC}		±20	mA
I_O	DC Output Sink Current		±25	mA
I_{CC}	DC Supply Current Per Supply Pin		50	mA
I_{GND}	DC Ground Current Per Supply Pin		-50	mA
T_{STG}	Storage Temperature Range		-65 to 150	°C
T_L	Lead Temperature, Soldering 10 Seconds		260	°C
T_J	Junction Temperature Under Bias		150	°C
V_{ESD}	ESD Classification	Human Body Model (2)	±4000	V
		Charged Device Model (3)	±1000	
I_{LU}	Latch-up Current Above V_{CC} and GND at 125°C (4)		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Note1: I_O absolute maximum rating must be observed.

Note2: HBM tested per JEDEC JS-001;

Note3: CDM tested per JEDEC JS-002;

Note4: Latch-up Current Maximum Rating tested per JEDEC JESD78F.

Thermal Characteristics

Symbol	Package	Ratings	Value	Unit
$R_{\theta JA}$	SC70-5	Thermal Characteristics, Thermal Resistance, Junction-to-Air	300	°C/W
	SOT23-5		250	
	DFN6		440	
P_D	SC70-5	Power Dissipation in Still Air at 85°C	215	mW
	SOT23-5		260	
	DFN6		150	

Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted)[5]

Symbol	Parameter	Min	Max	Unit
V_{CC}	Supply Voltage	4.5	5.5	V
V_I	Input Voltage	0	V_{CC}	V
V_O	Output Voltage	0	V_{CC}	V
I_{OH}	High-Level Output Current		-8	mA
I_{OL}	Low-Level Output Current		8	mA
$\Delta t/\Delta V$	Input Transition Rise or Fall Rate		20	ns/V
T_A	Operating Free-air Temperature	-40	125	°C

Note5: All unused inputs of the device must be held at VCC or GND to ensure proper device operation.

Electrical Characteristics

DC Electrical Characteristics

Symbol	Parameter	Condition	V_{CC} (V)	T_A = 25°C			-40°C ≤ T_A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
V_{IH}	High-Level Input Voltage		4.5	2			2		V
			5.5	2			2		
V_{IL}	Low-Level Input Voltage		4.5			0.8		0.8	V
			5.5			0.8		0.8	
V_{OH}	High-Level Output Voltage	I_{OH} = -50μA	4.5	4.4	4.5		4.4		V
		I_{OH} = -8mA	4.5	3.94			3.8		
V_{OL}	Low-Level Output Voltage	I_{OL} = 50μA	4.5			0.1		0.1	V
		I_{OL} = 8mA	4.5			0.36		0.44	
I_I	Input Current	V_I = 5.5V or GND	0~5.5			±0.1		±1	μA
I_{OZ}	OFF-state Output Current	V_O = V_{CC} or GND	5.5			±0.25		±2.5	uA
I_{CC}	Supply Current	V_I = V_{CC} or GND, I_O = 0mA	5.5			1.0		10	μA
ΔI_{CC} [6]	Change in Supply Current	One input at 3.4V, Other Inputs at V_{CC} or GND	5.5			1.35		1.5	mA
C_I	Input Capacitance	V_I = V_{CC} or GND	5		4	10			pF
C_O	Output Capacitance	V_O = V_{CC} or GND	5		10				pF

Note6: This is the increase in supply current for each input at one of the specified TTL voltage levels, rather than 0V or V_{CC}.

Switching Characteristics

$t_r = t_f$ = 3ns, over recommended operating free-air temperature range, V_{CC} = 5V ± 0.5V (unless otherwise noted) (see Figure3)

Symbol	Parameter	Condition	T_A = 25°C			-40°C ≤ T_A ≤ 125°C		Unit
			Min	Typ	Max	Min	Max	
t_{PLH}	Propagation Delay	C_L = 15pF		7	10	1.0	12	ns
t_{PHL}		C_L = 50pF		8.5	12	1.0	14	
t_{PZL}	Output Enable Time	C_L = 15pF		7.5	10.5	1.0	12	ns
t_{PZH}		C_L = 50pF		9	12	1.0	14	
t_{PLZ}	Output Disable Time	C_L = 15pF		8.1	11.5	1.0	13.5	ns
t_{PHZ}		C_L = 50pF		9.6	13.5	1.0	16	

Capacitance Characteristics

V_{CC} = 5V, T_A = 25°C

Symbol	Parameter	Condition	Typ	Unit
C_{PD} (7)	Power Dissipation Capacitance	No Load, f = 1MHz	14	pF

Note7: C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

f_o = output frequency in MHz;

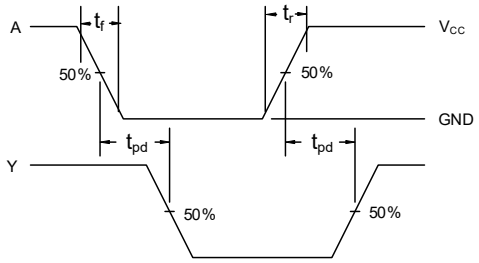
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

$\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

Parameter Measurement Information



PROPAGATION DELAYS:

$t_R = t_F$ = 5ns, 10% to 90%; f = 1MHZ;

Figure3. Switch Waveform

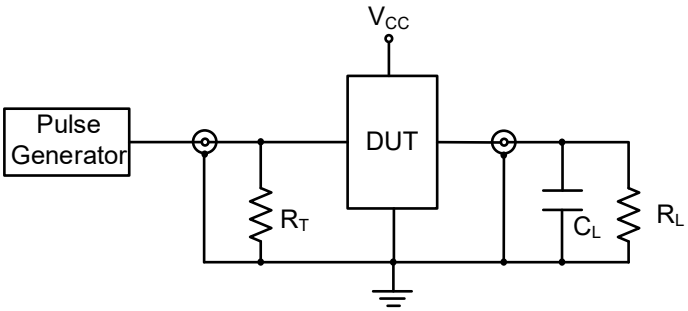
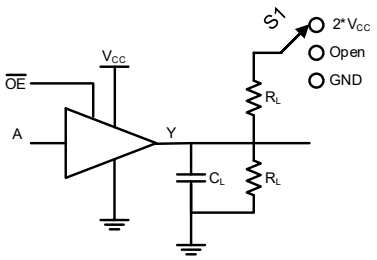


Figure4. Test Circuit R_T = 50Ω(typ)



Test	Switch
t_{pd}	Open
t_{PZL} t_{PLZ}	2*V_{CC}
t_{PZH} t_{PHZ}	GND

Figure5. Output Enable/Disable Time Test Circuit

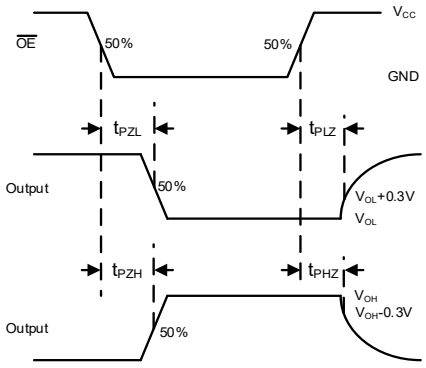
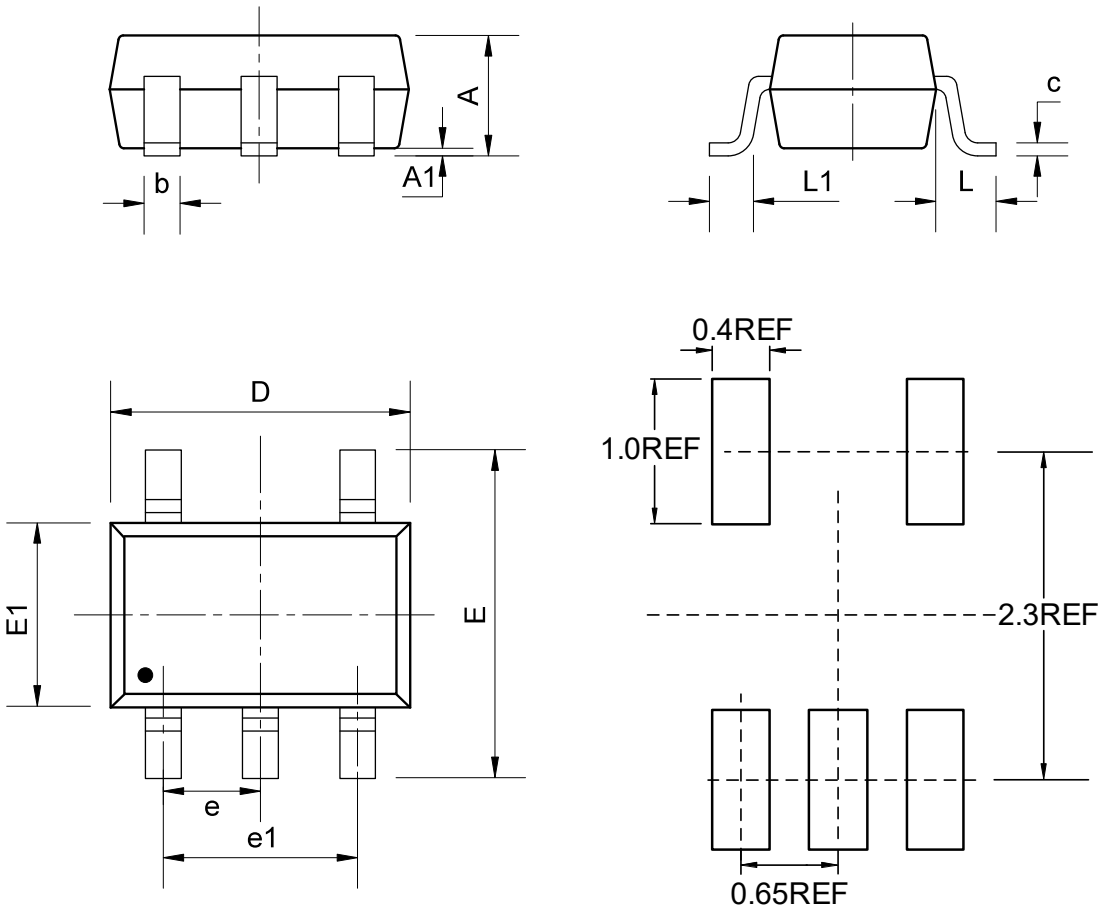


Figure6. Output Enable/Disable Waveform

Package Dimension

SC70-5 (1.3mm × 2.1mm)

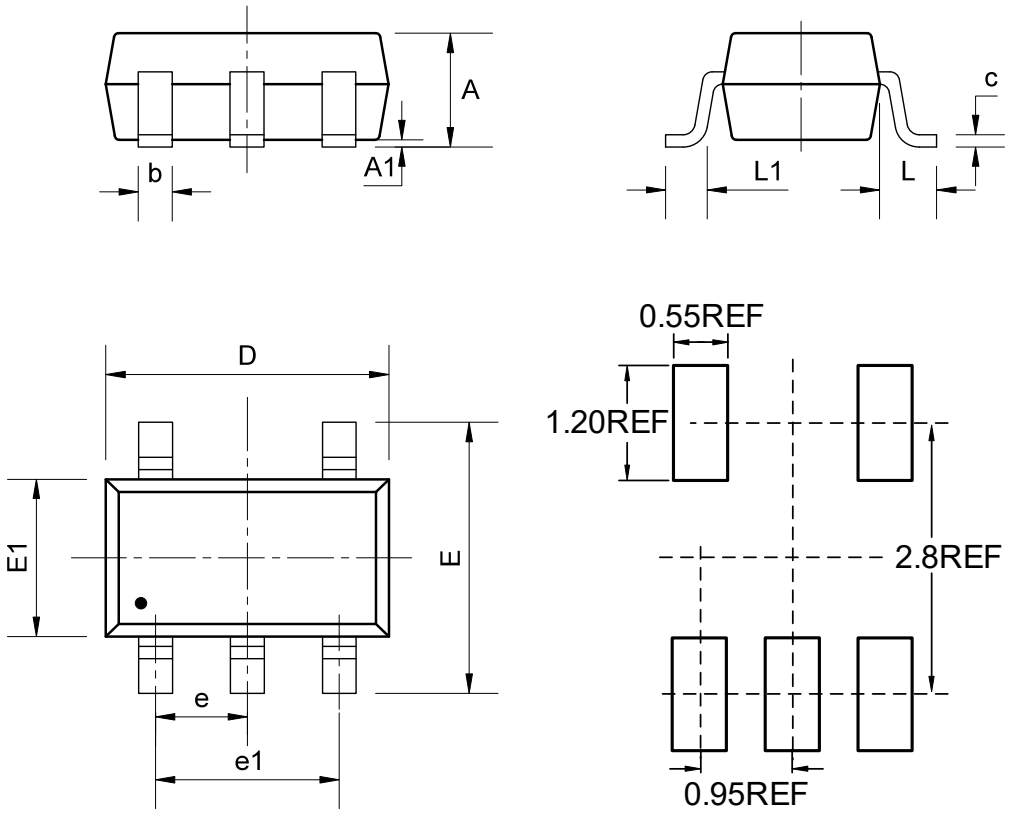


COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.10
A1	0.00	-	0.15
b	0.15	-	0.35
c	0.08	-	0.20
D	2.00	2.10	2.30
e	0.65BSC		
e1	1.30BSC		
E	2.15	2.30	2.50
E1	1.15	1.30	1.45
L	0.50REF		
L1	0.33REF		

SOT23-5 (1.6mm × 2.9mm)

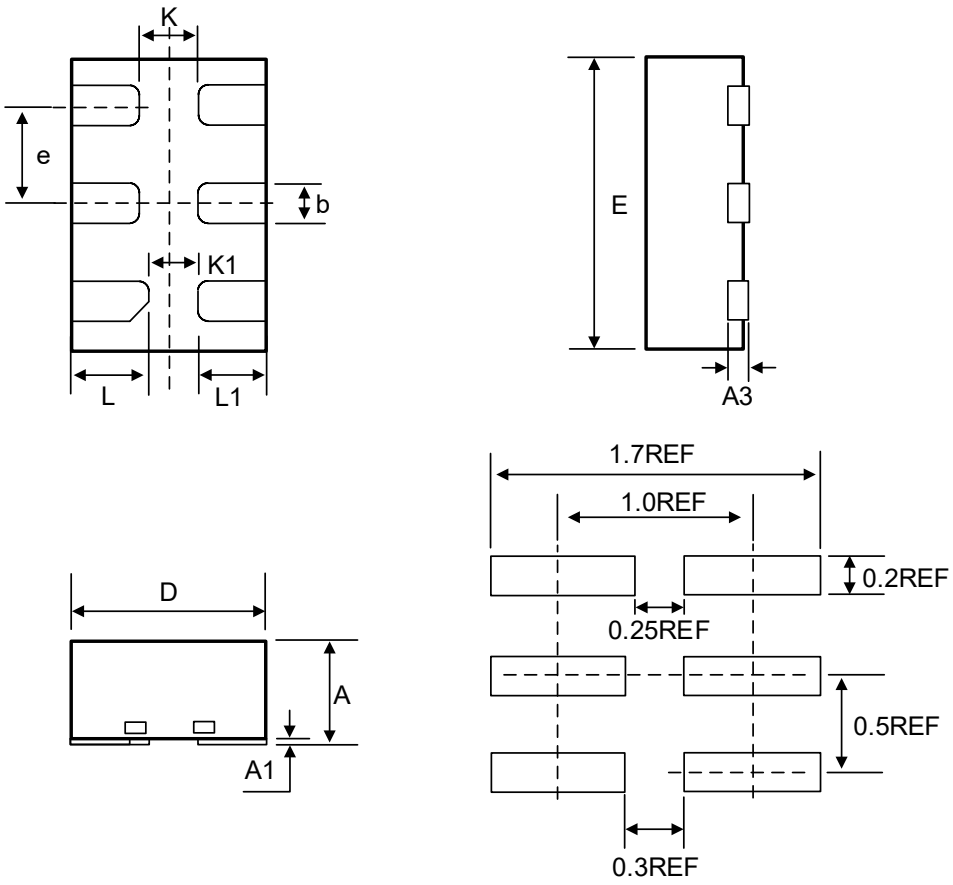


COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.45
A1	0.00	-	0.15
b	0.28	0.35	0.50
c	0.08	0.15	0.22
D	2.75	2.9	3.05
e	0.90	0.95	1.00
e1	1.80	1.90	2.00
E	2.60	2.80	3.00
E1	1.45	1.6	1.75
L	0.60REF		
L1	0.30	0.45	0.60

DFN6 (1.0mm × 1.5mm)



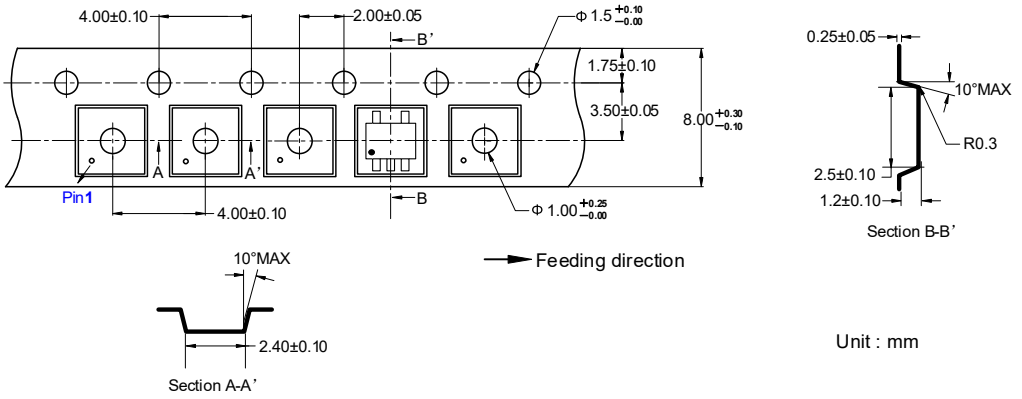
COMMON DIMENSIONS

(Unit: mm)

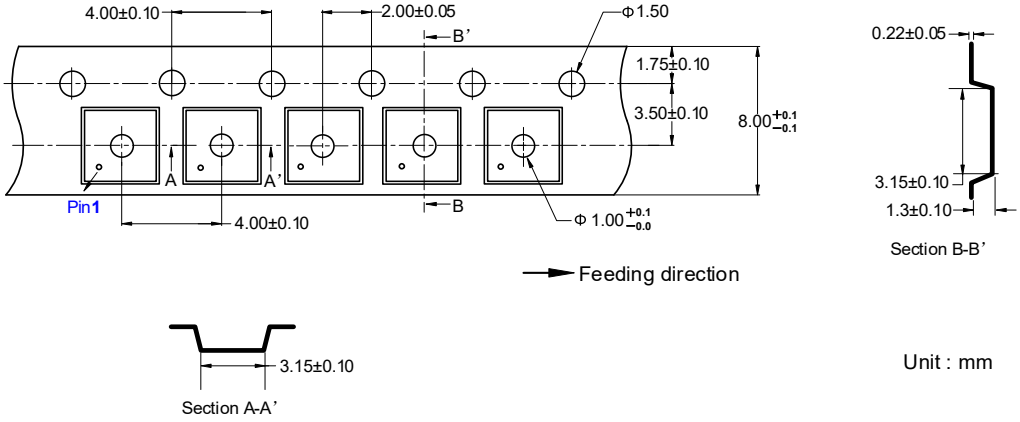
SYMBOL	MIN	NOM	MAX
A	0.50	--	0.60
A1	0.00	0.02	0.05
A3		0.10REF	
b	0.15	0.20	0.25
D	0.90	1.00	1.10
E	1.40	1.50	1.60
e		0.50BSC	
K		0.30REF	
K1		0.25REF	
L	0.35	0.40	0.45
L1	0.30	0.35	0.40

Tape Information

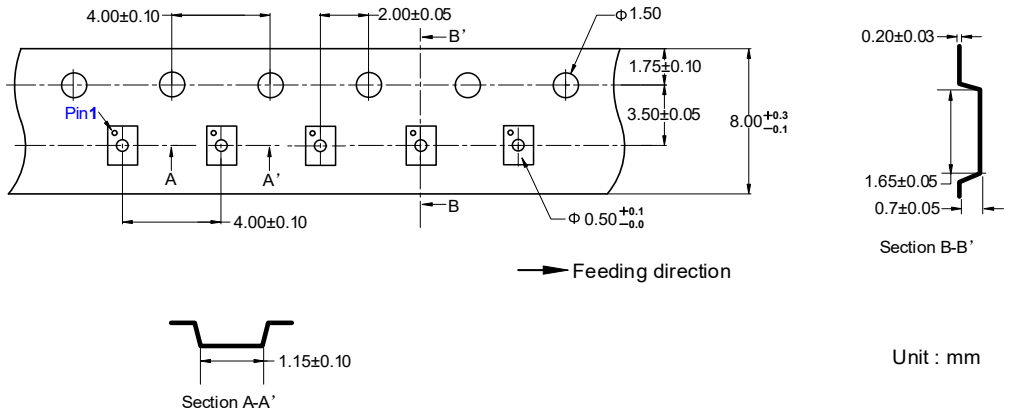
SC70-5 (1.3mm × 2.1mm)



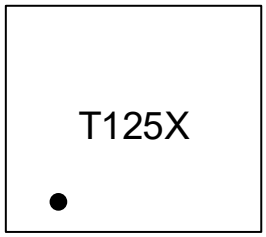
SOT23-5 (1.6mm × 2.9mm)



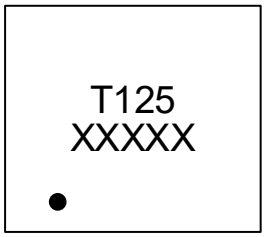
DFN6 (1.0mm × 1.5mm)



Marking Information



ET74AHCT1G125
T125 = Part Number
X = Tracking Number



ET74AHCT1G125T
T125 = Part Number
XXXXX = Tracking Number

Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
0.0	2025-10-28	Preliminary Version	Wang anran	Yang xiaoxu	Liu jiaying
1.0	2026-03-20	Official Version	Xu tao	Yang xiaoxu	Liu jiaying
1.0.a	2026-04-21	Update Format	Xu tao	Yang xiaoxu	Liu jiaying