Single Inverter with Open Drain Output

General Description

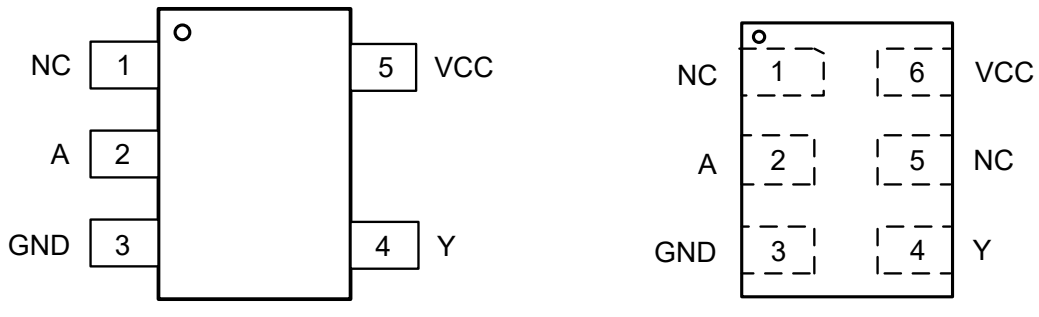
The ET74LVC1G06 is a high-performance single inverter with open drain outputs operating from a 1.65V to 5.5V supply. The Output stage is open drain with Over Voltage Tolerance. This allows the ET74LVC1G06 to be used to interface 5.0V circuits to circuits of any voltage between 0 and 6.5V.

Features

- Extremely High Speed: t_{PD} = 2.5ns (Typical) at V_{CC} = 5.0V
- Designed for 1.65V to 5.5V V_{CC} Operation, CMOS Compatible
- Over Voltage Tolerant Inputs V_I may be Between 0V and 6.5V for V_{CC} Between 0.5V and 5.5V
- 32mA Output Sink Capability, Pull-up may Be between 0 and 6.5V
- Near Zero Static Supply Current Substantially Reduces System Power Requirements
- These Devices are Pb-Free, Halogen Free/BFR Free and RoHS Compliant in Configuration
- ESD Protection Complies with JESD22 Standard
  -- HBM: ±4000V Pass (JEDEC JS-001)
  -- CDM: ±1000V Pass (JEDEC JS-002)
- Latch-up Performance Exceeds ±100mA per JEDEC JESD78F
- Part No. and Package Information

Part No.	Package	Packing Option	MSL
ET74LVC1G06	SC70-5 (1.3mm×2.1mm)	Tape and Reel, 3K/Reel	1
ET74LVC1G06T	SOT23-5 (1.6mm×2.9mm)	Tape and Reel, 3K/Reel	3
ET74LVC1G06Y	DFN6 (1.0mm×1.5mm)	Tape and Reel, 3K/Reel	1

Pin Configuration



SC70-5/SOT23-5 DFN6

Figure1. Pin Configuration

Pin Function

SC70-5/SOT23-5

Pin No.	Pin Name	Function
1	NC	No Connect
2	A	Input
3	GND	Ground
4	Y	Output
5	VCC	Supply Voltage

DFN6

Pin No.	Pin Name	Function
1	NC	No Connect
2	A	Input
3	GND	Ground
4	Y	Output
5	NC	No Connect
6	VCC	Supply Voltage

Block Diagram

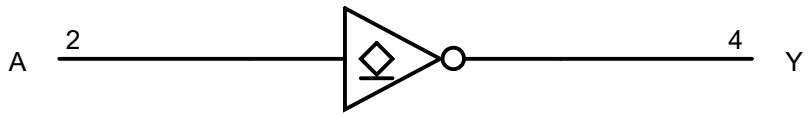


Figure2. Logic Symbol

Functional Table

Input A	Output Y
L	Z
H	L

Absolute Maximum Ratings

Symbol	Parameter		Value	Unit
V_{CC}	DC Supply Voltage		-0.5 to 6.5	V
V_I	DC Input Voltage[1]		$-0.5 \le V_I \le 6.5$	V
V_O	DC Output Voltage Output in Higher or Low State		-0.5 to V_{CC} + 0.5	V
I_{IK}	DC Input Diode Current, V_I < GND		-50	mA
I_{OK}	DC Output Diode Current, V_O < GND, V_O > V_{CC}		±50	mA
I_O	DC Output Sink Current		±50	mA
I_{CC}	DC Supply Current per Supply Pin		±100	mA
I_{GND}	DC Ground Current per Supply Pin		±100	mA
T_{STG}	Storage Temperature Range		-65 to 150	°C
T_L	Lead Temperature, Soldering 10 Seconds		260	°C
T_J	Max Junction Temperature		150	°C
V_{ESD}	ESD Classification	Human Body Model [2]	±4000	V
		Charged Device Model [3]	±1000	
I_{LU}	Max Latch up Current Above V_{CC} and GND at 125°C [4]		±100	mA

Stresses exceeding those listed in this table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Note1: I_O absolute maximum rating must be observed.

Note2: HBM tested per JEDEC JS-001;

Note3: CDM tested per JEDEC JS-002;

Note4: Latch Up Current Maximum Rating tested per JEDEC JESD78F.

Thermal Characteristics

Symbol	Package	Ratings	Value	Unit
$R_{\theta JA}$	SC70-5	Thermal Characteristics, Thermal Resistance, Junction-to-Air	300	°C/W
	SOT23-5		250	
	DFN6(1.0mm × 1.5mm)		440	
P_D	SC70-5	Power Dissipation in Still Air at 85°C	215	mW
	SOT23-5		260	
	DFN6(1.0mm × 1.5mm)		150	

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V_{CC}	DC Supply Voltage	1.65	5.5	V
	Operating Date Retention	1.5	5.5	
V_I	DC Input Voltage	0	V_{CC}	V
V_O	DC Output Voltage(High or Low State)	0	V_{CC}	V
T_A	Operating Temperature Range	-40	125	°C

Electrical Characteristics

DC Electrical Characteristics

(R_{PULL} = 1kΩ, Connect V_O to V_{CC})

Symbol	Parameter	Condition	V_{CC}(V)	T_A = 25°C			-40°C ≤ T_A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
V_{IH}	High-Level Input Voltage		1.65~1.95 2.3~5.5	0.75V_{CC} 0.7V_{CC}			0.75V_{CC} 0.7V_{CC}		V
V_{IL}	Low-Level Input Voltage		1.65~1.95 2.3~5.5			0.25V_{CC} 0.29V_{CC}		0.25V_{CC} 0.29V_{CC}	V
V_{OL}	Low-Level Output Voltage V_{IN} = V_{IH}	I_{OL} = 100uA	1.65~5.5		0.0	0.1		0.1	V
		I_{OL} = 3mA	1.65		0.08	0.24		0.24	
		I_{OL} = 8mA	2.3		0.20	0.3		0.3	
		I_{OL} = 12mA	2.7		0.22	0.4		0.4	
		I_{OL} = 16mA	3.0		0.28	0.4		0.4	
		I_{OL} = 24mA	3.0		0.38	0.55		0.55	
		I_{OL} = 32mA	4.5		0.42	0.55		0.55	
I_{IN}	Input Leakage Current	V_I = 5.5V or GND	0~5.5		±0.1			±1.0	uA
I_{OFF}	Power Off Leakage Current	V_I = 5.5V or V_O = 5.5V	0			1		10	uA
I_{CC}	Quiescent Supply Current	V_I = 5.5V or GND	5.5					10	uA

AC Electrical Characteristics

$t_r = t_f$ = 3ns;

Symbol	Parameter	Condition	V_{CC}(V)	T_A = 25°C			-40°C ≤ T_A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
t_{PLZ} t_{PZL}	Propagation Delay (Figure 3 & 4)	R_L = 1kΩ, C_L = 15pF	1.65	3.0	9.5	12	3.0	16.0	ns
			1.8	3.0	8.0	11	3.0	15.0	
		R_L = 1kΩ, C_L = 15pF	2.3~2.7	1.0	5.0	7.5	1.0	11.0	
		R_L = 1kΩ, C_L = 15pF	3.0~3.6	1.0	3.5	6.5	1.0	9.0	
		R_L = 500Ω, C_L = 50pF		1.5	4.5	7.5	1.5	10.0	
		R_L = 1kΩ, C_L = 15pF	4.5~5.5	0.5	2.5	5.0	0.5	7.5	
		R_L = 500Ω, C_L = 50pF		0.8	3.2	6.0	0.8	8.5	

Capacitance Characteristics

Symbol	Parameter	Condition	Typ	Unit
C_{IN}	Input Capacitance	V_{CC} = 5.5V, V_I = 0V or V_{CC}	2.8	pF
C_{PD}	Power Dissipation Capacitance (5)	10MHz, V_{CC} = 3.3V, V_I = 0V or V_{CC}	2.5	pF
		10MHz, V_{CC} = 5.5V, V_I = 0V or V_{CC}	3.0	

Note5: C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

$\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

AC Test Circuit

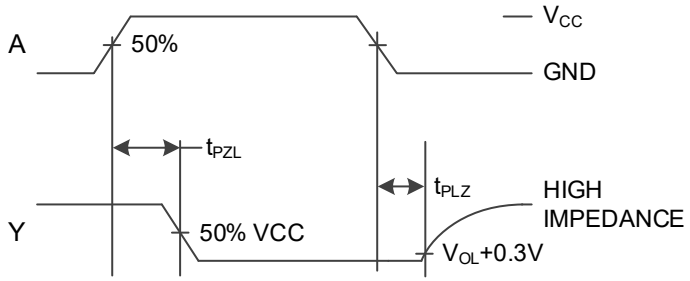
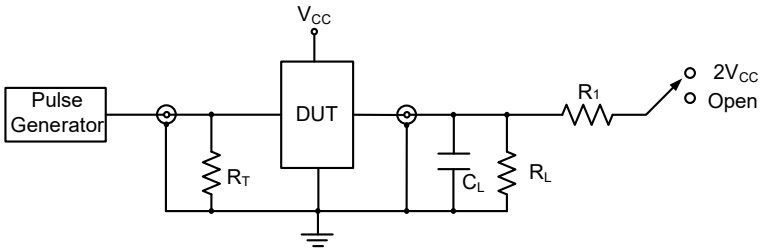


Figure3. Switching Waveforms

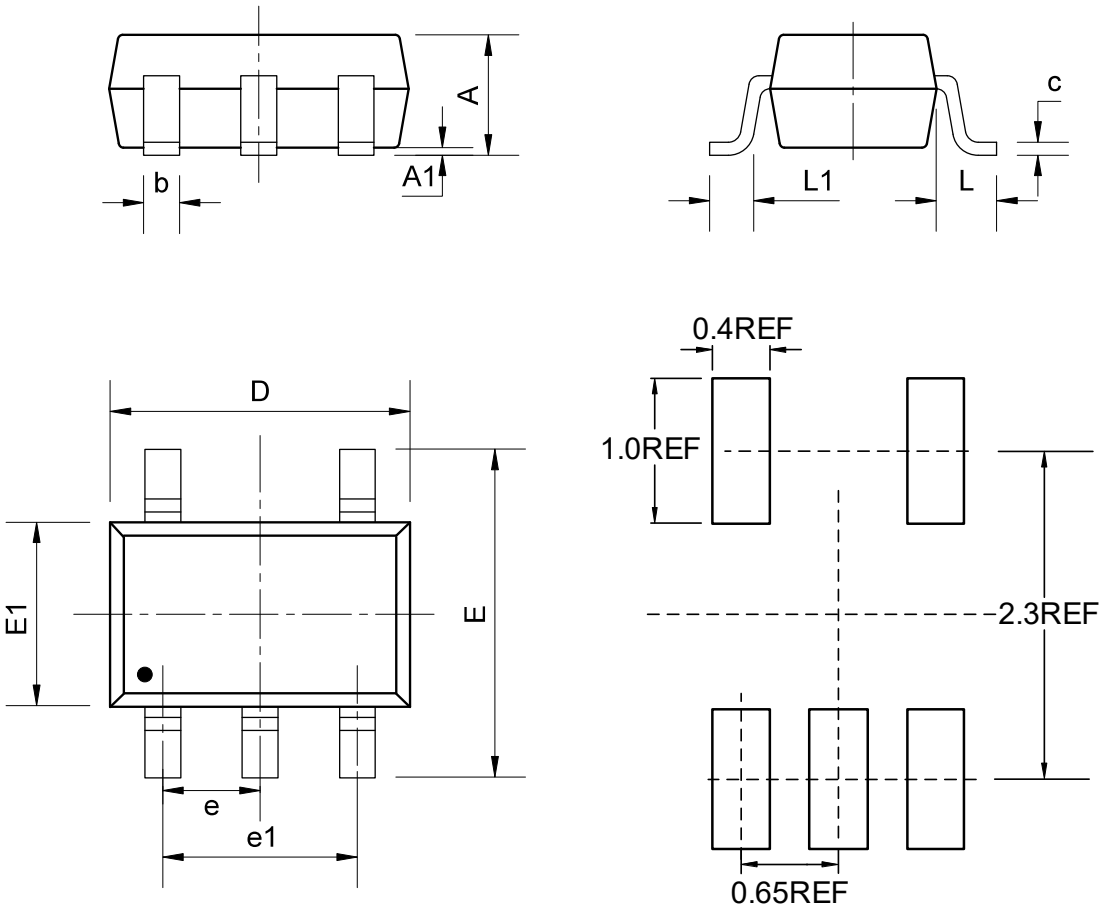


R_T = 50Ω(Typ)

Figure4. Test Circuit

Package Dimension

SC70-5 (1.3mm × 2.1mm)

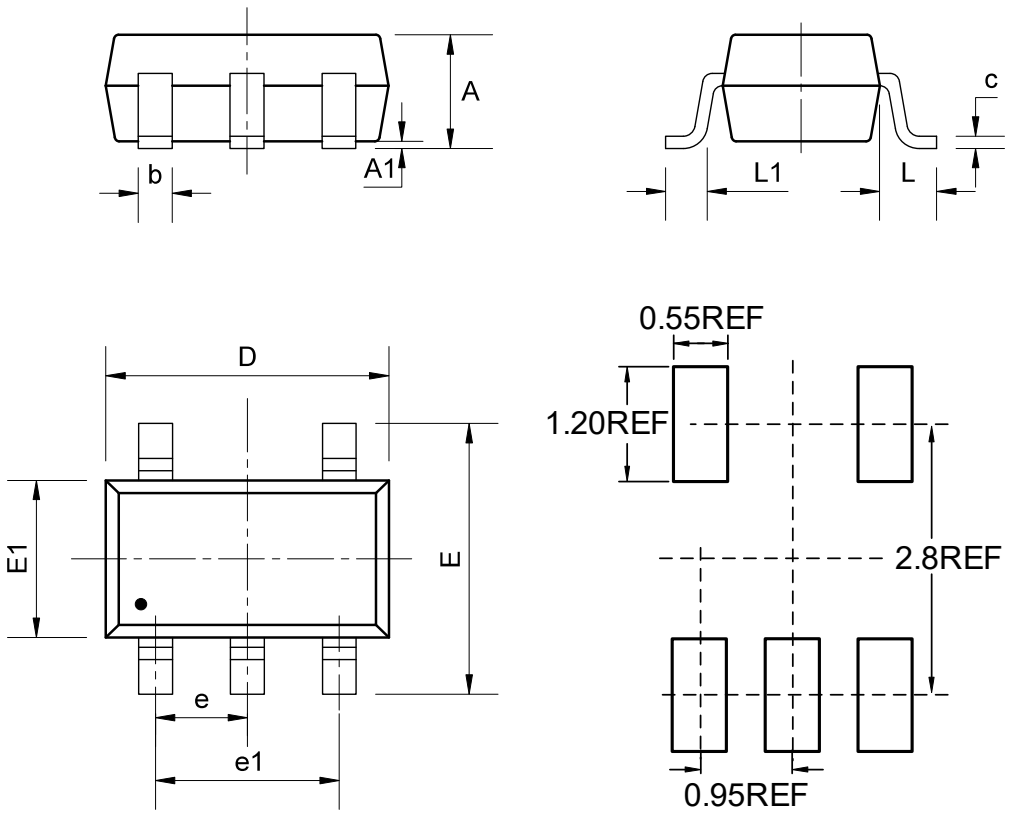


COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.10
A1	0.00	-	0.15
b	0.15	-	0.35
c	0.08	-	0.20
D	2.00	2.10	2.30
e	0.65BSC		
e1	1.30BSC		
E	2.15	2.30	2.50
E1	1.15	1.30	1.45
L	0.50REF		
L1	0.33REF		

SOT23-5 (1.6mm × 2.9mm)

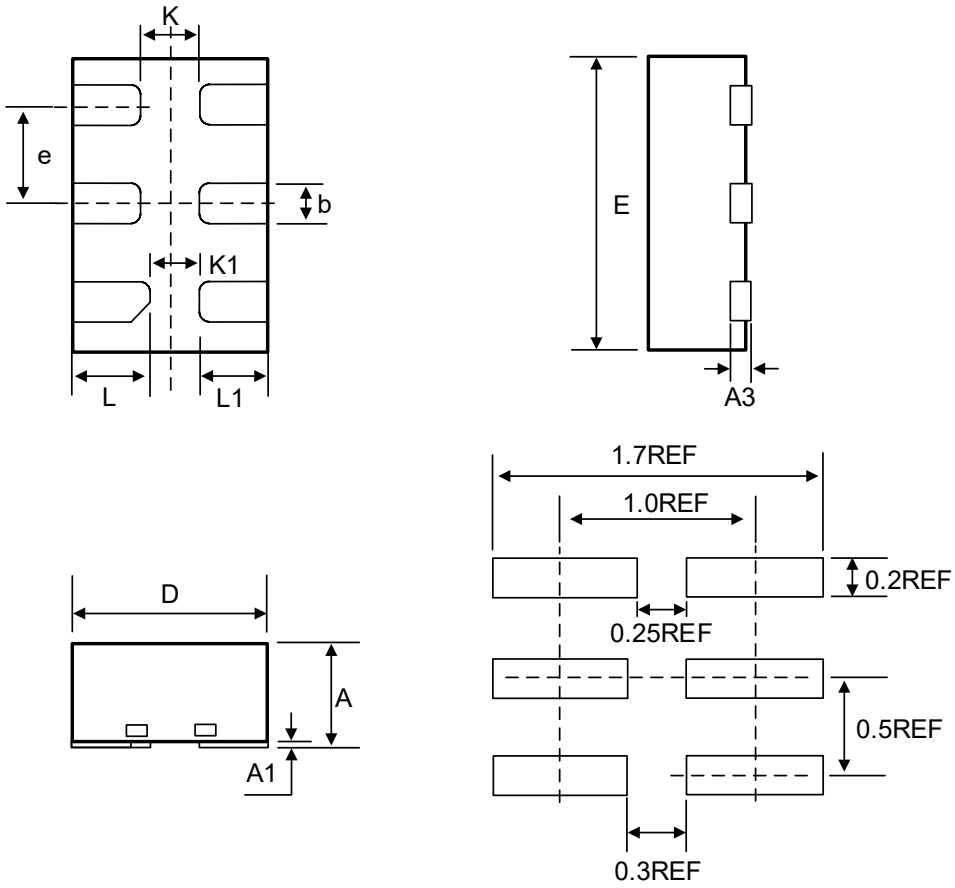


COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.45
A1	0.00	-	0.15
b	0.28	0.35	0.50
c	0.08	0.15	0.22
D	2.75	2.9	3.05
e	0.90	0.95	1.00
e1	1.80	1.90	2.00
E	2.60	2.80	3.00
E1	1.45	1.6	1.75
L	0.60REF		
L1	0.30	0.45	0.60

DFN6 (1.0mm × 1.5mm)



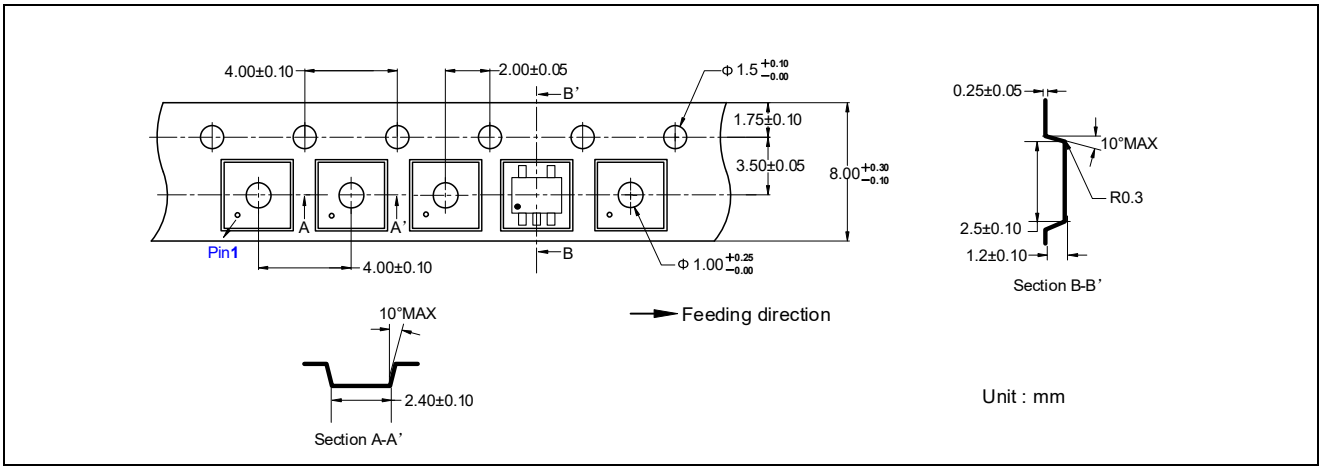
COMMON DIMENSIONS

(Unit: mm)

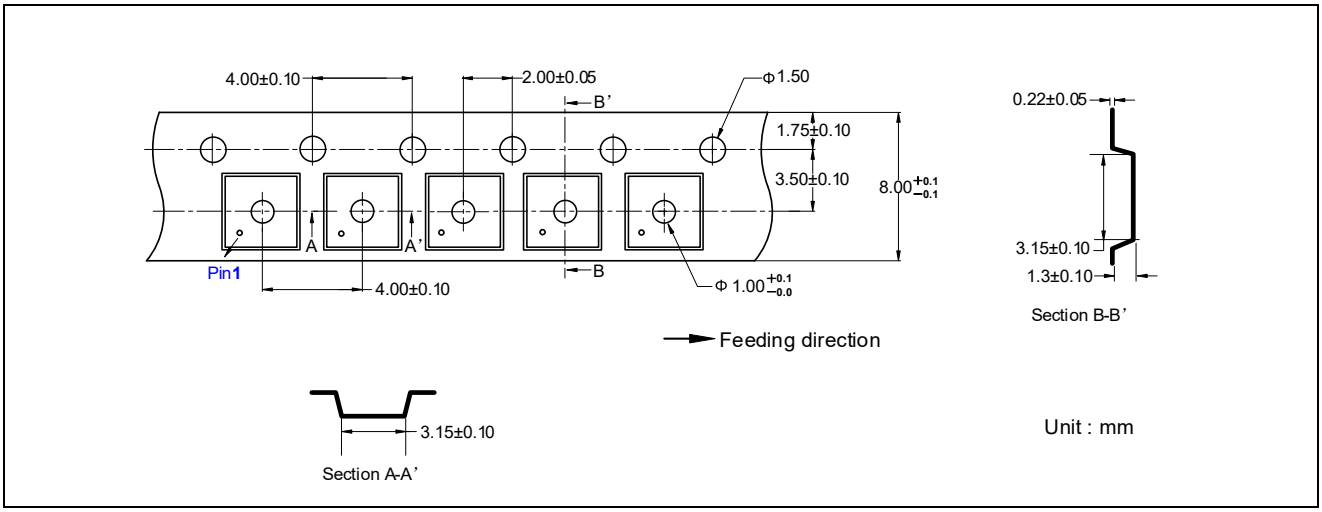
SYMBOL	MIN	NOM	MAX
A	0.50	--	0.60
A1	0.00	0.02	0.05
A3		0.10REF	
b	0.15	0.20	0.25
D	0.90	1.00	1.10
E	1.40	1.50	1.60
e		0.50BSC	
K		0.30REF	
K1		0.25REF	
L	0.35	0.40	0.45
L1	0.30	0.35	0.40

Tape Information

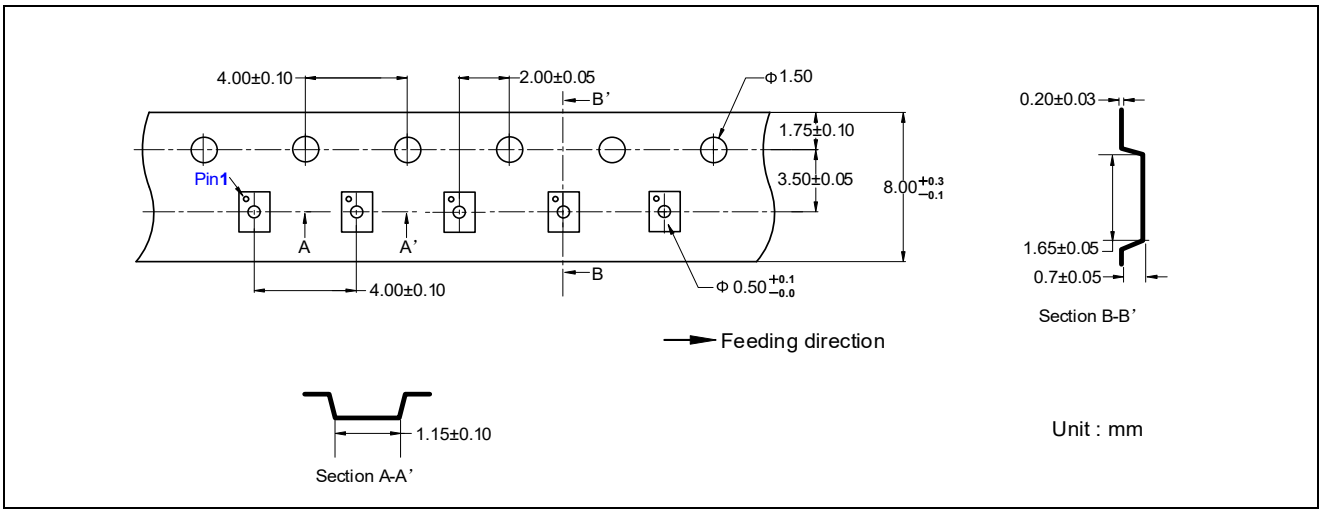
SC70-6 (1.3mm × 2.1mm)



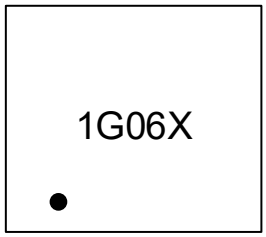
SOT23-6 (1.6mm × 2.9mm)



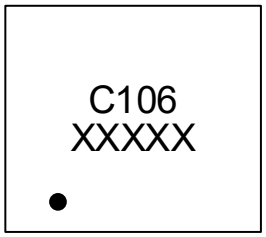
DFN6 (1.0mm × 1.5mm)



Marking Information



ET74LVC1G06
1G06 = Part Number
X = Tracking Number



ET74LVC1G06T
C106 = Part Number
XXXXX = Tracking Number

Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2017-07-19	Original Version	Ma yongjian	Ma yongjian	Liu jiaying
1.1	2022-08-02	Update Typesetting	Shi bo	Shi bo	Shi bo
1.2	2022-10-14	Update format and Thermal Characteristics and C_{PD}	Wu han	Shi bo	Shi bo
1.3	2023-12-07	Update Typeset /ESD	Shi bo	Shi bo	Shi bo
1.4	2025-05-27	Add Packing Option	Yang xiaoxu	Yang xiaoxu	Liu jiaying
1.4.a	2025-09-18	Update Switching Waveform	Yang xiaoxu	Yang xiaoxu	Liu jiaying
1.4.b	2025-12-25	Update Format, Add Tape and Marking	Xu tao	Yang xiaoxu	Liu jiaying