Push Button Reboot Controller

General Description

ET9820 is a timer reset IC that supplies a reboot signal to the system when “L” voltage is input into the SW1, SW2 pins for a set time (reboot delay time) using two switches (physical buttons).

By changing the external resistance R_T, the reboot delay time (T_{DL}) can be set from 0.1s to 20s.

After the reboot signal (T_{RSTB}) is output for 15ms (TYP), the IC returns to the steady state automatically.

Quiescent current in standby mode is 0.01μA (TYP.), which is very small and leads to a longer battery drive time. The small DFN package reduces the mounting space.

In order to prevent malfunctioning of the IC, the UVLO function is equipped as a protective function.

Features

- Input Voltage Range: 2.5V ~ 5.5V
- Low power Consumption: 0.01μA (Stand-by, TYP.)
- Output Configuration: Nch open drain
- RSTB Pin SINK Current: 30mA (V_{RSTBL}=0.3V.)
- Reboot Delay Time: 500ms/ 1s/ 1.5s ±20% (Adjustable by the external resistor R_T)
- Reboot Time:15ms ±33%
- Operating Ambient Temperature: -40°C ~ +85°C

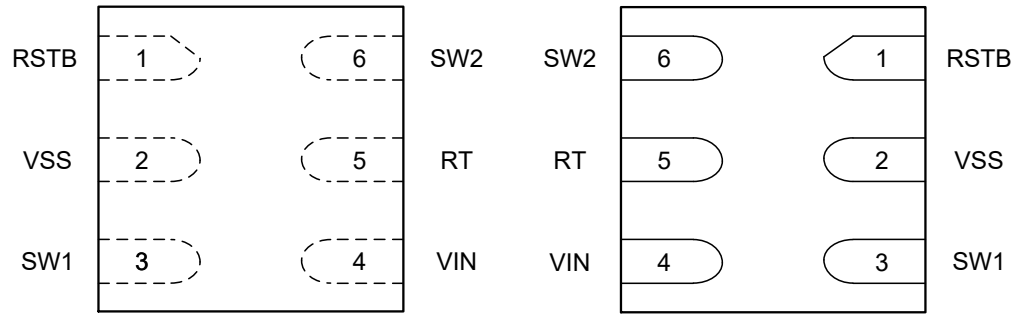
Device Information

Part No.	Package	Packing Option	MSL
ET9820	DFN6 (1.0mm×1.0mm)	Tape and Reel ,5K	Level 1

Applications

- Wearable Devices
- Portable Music Players
- Portable Video Game Players
- Wireless Headsets
- Mobile Communication Devices
- Smart Phones, Feature Phones
- Various applications equipped with buttons

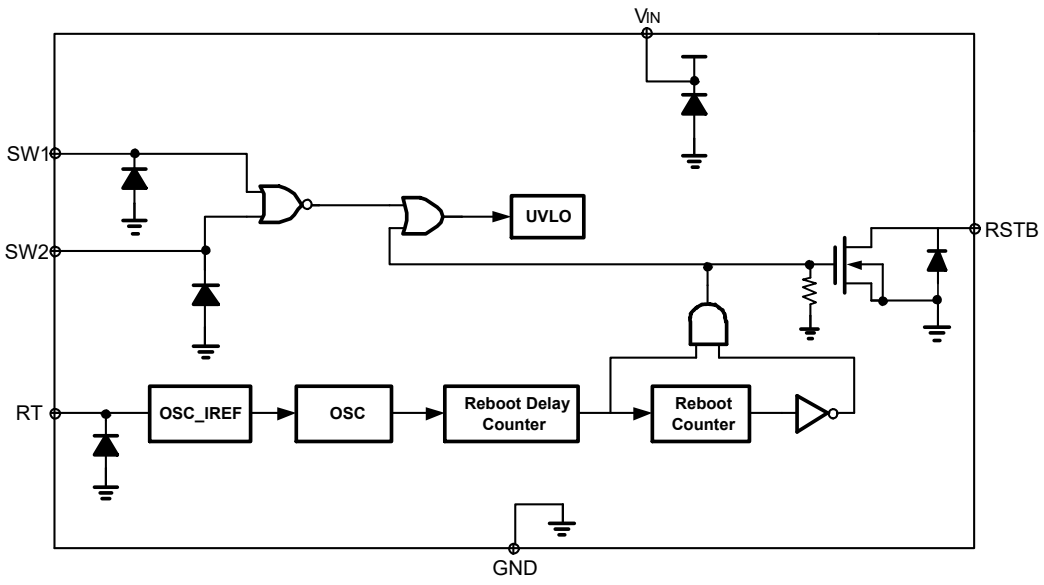
Pin Configuration



Pin Function

PIN NUMBER	PIN NAME	FUNCTIONS
1	RSTB	Reboot Signal Output Pin
2	VSS	Ground Pin
3	SW1	Switch Signal Input Pin (This pin must not be left floating)
4	VIN	Power Input Pin
5	RT	T_{DL} adjusting resistor connection Pin (This pin must not be left floating, need an external pull-down resistor connected to ground to determine T_{DL})
6	SW2	Switch Signal Input Pin (This pin must not be left floating)

Block Diagram



*The above diodes are elctrostatic protection diodes and parasitic diodes.

Function Table

Pin Name	VDD	INPUTS		OUTPUTS
	VIN	SW1	SW2	RSTB
Logic Table	5V	H	H	H
		L	H	H
		H	L	H
		L	L	L
	0V	H	H	H
		L	H	H
		H	L	H
		L	L	H

Operation Explanation

ET9820 supplies a reboot signal (T_{RSTB} below) to the system by inputting “L” voltage into the SW1, SW2 pins for the set time (reboot delay time) using two switches (buttons).

On the ET9820, a reboot delay time (T_{DL} below) is set by connecting a resistance (R_T below) to the RT pin.

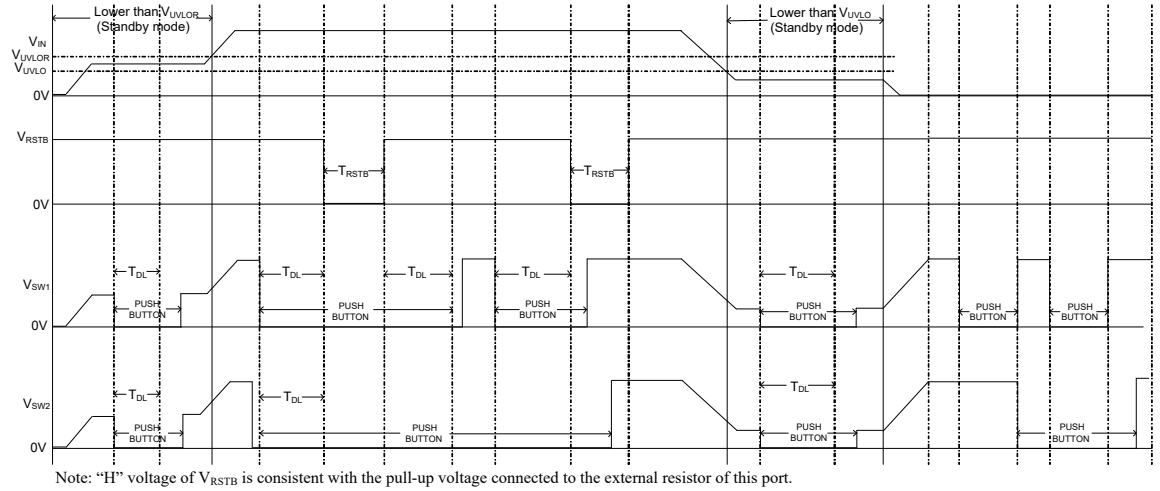
T_{DL} is determined by the following calculation formula:

$$R_T(k\Omega) = \frac{T_{DL}(s) - 0.000480}{0.006244}$$

* T_{DL} can be set within the range 0.1s to 20s.

e.g.) When T_{DL}= 0.5s, R_T=80kΩ;

when T_{DL}= 1s, R_T=160kΩ;
when T_{DL}= 1.5s, R_T=240kΩ.
when T_{DL}= 10s, R_T=1.6MΩ.



Note: “H” voltage of V_{RSTB} is consistent with the pull-up voltage connected to the external resistor of this port.

Fig.1 Timing Chart

Details of each circuit part are as follows.

SW1 pin, SW2 pin

By inputting “L” voltage into both of these pins during the T_{DL} interval, the reboot signal is output from the RSTB pin. Both pins must not be left floating.

The UVLO circuit is activated by inputting “L” voltage into both the SW1 pin and the SW2 pin.

RT pin

RT is connected to this pin to set T_{DL}.

UVLO

This circuit prevents malfunctioning of the IC and allows the internal circuit to operate normally.

When the “L” voltage is detected input into both the SW1 and SW2 pins, the VIN pin voltage is monitored.

When the VIN pin voltage is higher than the UVLO release voltage, a signal that allows the internal circuit to operate is output by the UVLO circuit.

When the VIN pin voltage is lower than the UVLO detect voltage, a signal that puts the internal circuit in the standby state is output by the UVLO circuit.

When “H” voltage is input into the SW1 pin or SW2 pin (or both), the UVLO circuit does not operate and the internal circuitry enters the standby state.

OSC IREF

This is a current reference circuit for the OSC circuit which is set by RT connected to the RT pin.

OSC

This is an oscillation circuit which uses the reference current of the OSC IREF circuit.

This circuit outputs an oscillation pulse signal that activates the Reboot Delay Counter and Reboot Counter.

Reboot Delay Counter

This circuit counts the oscillation pulse signal generated by the OSC circuit and generates the T_{DL}.

When the count is over, the circuit outputs a signal that places the RSTB pin voltage at “L” level and activates the Reboot Counter.

If a signal from the UVLO circuit that changes the state to the standby state is detected during the count, the count returns to the initial state.

Reboot Counter

By counting the oscillation pulse signal generated by the OSC circuit, the reboot time (T_{RSTB}) is generated. The count starts when it detects the start signal that is output from the Reboot Delay Counter.

If “H” voltage is input into the SW1 pin or SW2 pin (or both) during the counting process, the internal circuit does not change to the standby state until the count ends.

If a change-to-standby signal, which is output from the UVLO circuit, is detected during the counting process, the count returns to the initial state.

When the count ends on the ET9820, the circuit outputs a signal that places the RSTB pin in a high-impedance state.

Output Driver

The ET9820 is an N-ch open drain output driver. The driver is in the OFF state when the Reboot Counter is not operating.

After the reboot signal output is completed, “H” voltage must be input into the SW1 pin and SW2 pin (or both) to activate the reboot signal output again. When “L” voltage is input into both the SW1 and SW2 pins after “H” voltage, the reboot signal is output from the RSTB pin and then T_{DL} elapses.

Absolute Maximum Ratings

Absolute maximum ratings are limited values to be applied individually, and beyond which the serviceability of the circuit may be impaired. Functional operability is not necessarily implied. Exposure to absolute maximum rating conditions for an extended period of time may affect device reliability.

PARAMETER	SYMBOL	MIN	MAX	UNITS
VIN Pin Voltage	V_{IN}	-0.3	6	V
SW1 Pin Voltage	V_{SW1}	-0.3	6	V
SW2 Pin Voltage	V_{SW2}	-0.3	6	V
RT Pin Voltage	V_{RT}	-0.3	V_{IN}+0.3 or 6.0 (1)	V
RSTB Pin Voltage	V_{RSTB}	-0.3	6	V
RSTB Pin SINK Current	I_{SINK}	40		mA
Power Dissipation	Pd	600		mW
Operating Ambient Temperature	T_{opr}	-40	85	°C
Storage Temperature	T_{stg}	-55	125	°C

Each voltage rating is based on the reference GND.

Note1: The maximum value is the lower of either V_{IN} + 0.3 or +6.0V.

Electrical Characteristics

Unless otherwise stated, T_A=25 °C, the reference is GND, V_{IN}=5V, R_T=80kΩ.

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage Range	V_{IN}	-	2.5	-	5.5	V
UVLO Release Voltage	V_{UVLOR}	V_{IN}=SWEEP (step up), V_{SW1}=GND, V_{SW2}=GND	-	2.1	2.4	V
UVLO Detect Voltage(2)	V_{UVLO}	V_{IN}=SWEEP (step down), V_{SW1} =GND, V_{SW2}=GND	1.6	1.9	-	V
Quiescent Current	I_Q	V_{IN}=5V, V_{SW1}=V_{SW2}=1.8V	-	1	3	μA
		V_{IN}=5V, V_{SW1}=V_{SW2}=5V	-	0.01	0.1	μA
Operating Current(3)	I_{DD}	V_{SW1}=GND, V_{SW2}=GND	50	100	200	μA
Reboot Delay Time(4)	T_{DL}	V_{SW1}=GND, V_{SW2}=GND, R_T=80kΩ	-20%		+20%	
			400	500	600	ms
Reboot Time(5)	T_{RSTB}	V_{SW1}=GND, V_{SW2}=GND *After T_{DL}	-33%		+33%	
			10	15	20	ms
RSTB Pin SINK Current	I_{SINK}	V_{RSTB}=0.3V, V_{SW1}=GND, V_{SW2}=GND	30	-		mA
SW1 Pin "H" Voltage	V_{SW1H}	V_{IN}=5.5V	1.6	-	5.5	V
SW1 Pin "L" Voltage	V_{SW1L}	V_{IN}=5.5V	GND	-	0.3	V
SW1 Pin "H" Current	I_{SW1H}	V_{IN}=5.5V, V_{SW1}=5.5V	-0.1	-	0.1	μA
SW1 Pin "L" Current	I_{SW1L}	V_{IN}=5.5V, V_{SW1}=GND	-0.1	-	0.1	μA
SW2 Pin "H" Voltage	V_{SW2H}	V_{IN}=5.5V	1.6	-	5.5	V
SW2 Pin "L" Voltage	V_{SW2L}	V_{IN}=5.5V	GND	-	0.3	V
SW2 Pin "H" Current	I_{SW2H}	V_{IN}=5.5V, V_{SW2}=5.5V	-0.1	-	0.1	μA
SW2 Pin "L" Current	I_{SW2L}	V_{IN}=5.5V, V_{SW2}=GND	-0.1	-	0.1	μA

NOTE2: The UVLO detect voltage is less than the UVLO release voltage.

NOTE3: Quiescent current until the reboot signal is output in the circuit operation state with the SW1, SW2 pin at "L" level.

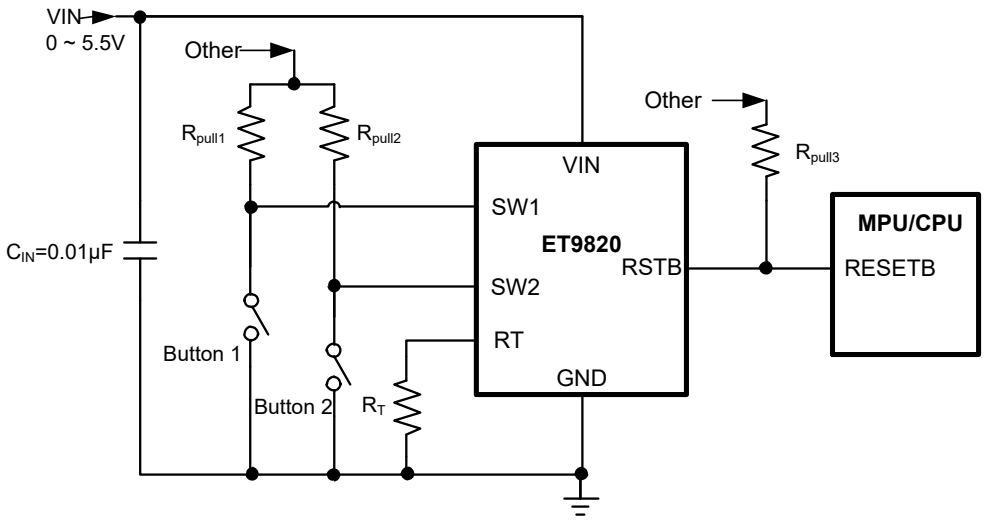
NOTE4: Time from change of both SW1 and SW2 pin voltages to "L" level until the RSTB pin outputs "L" level(6).

NOTE5: Time from change of RSTB pin to "L" level until RSTB pin changes to "H" level(7).

NOTE6: RSTB pin "L" level is as follows: Applied voltage × 0.1.

NOTE7: RSTB pin "H" level is as follows: Applied voltage × 0.9.

Application Circuit

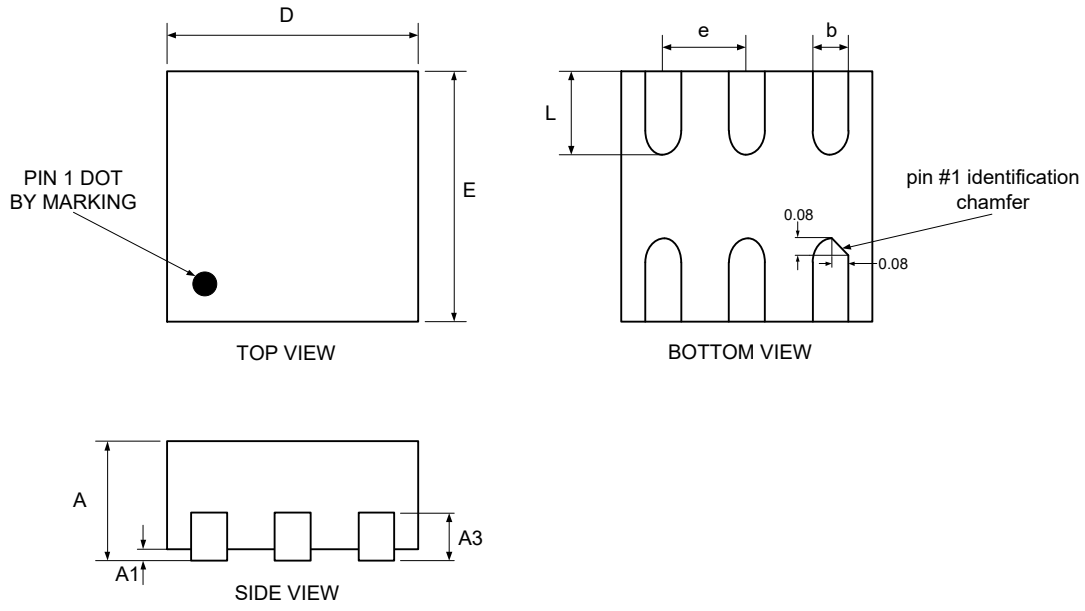


Two Button Solution

On the ET9820, connect a capacitor of at least 0.01uF between VIN-GND near the IC as needed to decouple input power supply glitch and noise.

Package Dimension

DFN1x1-6L

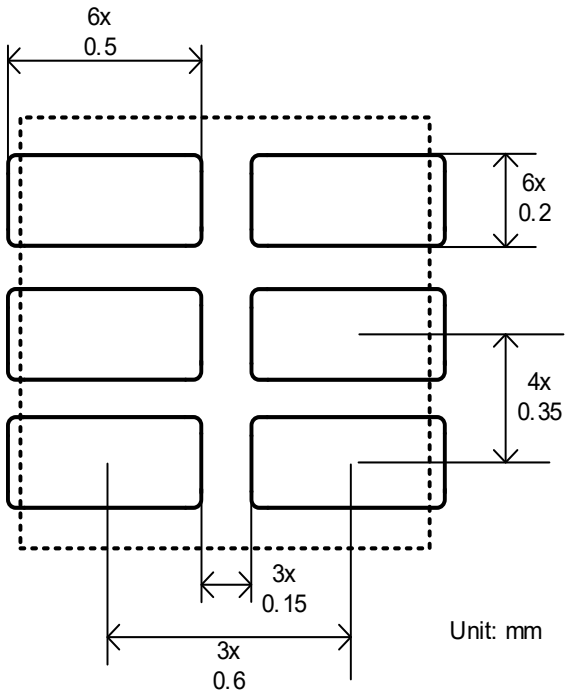


COMMON DIMENSIONS

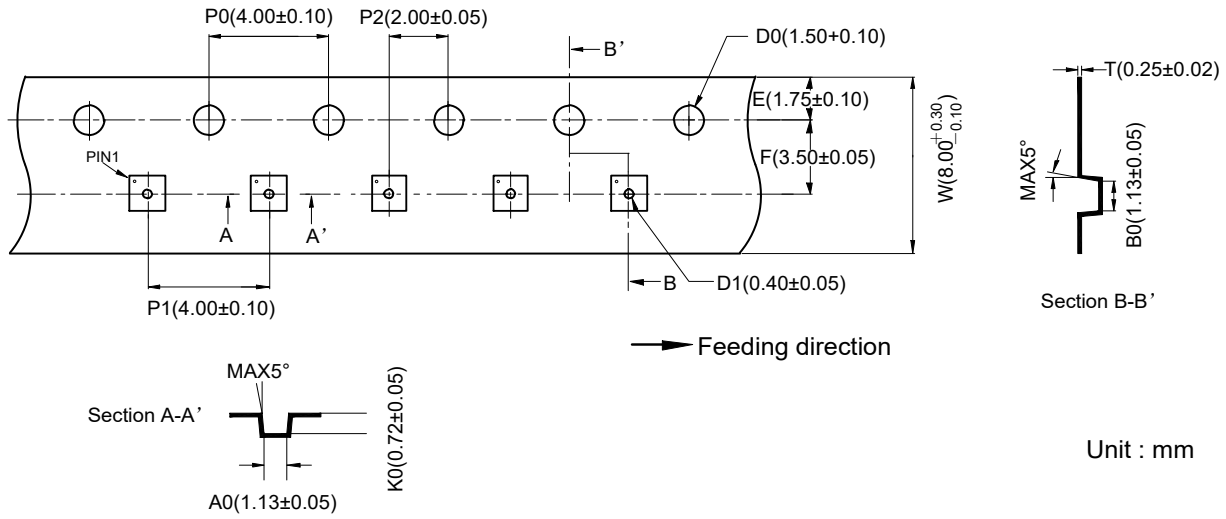
(UNITS OF MEASURE = MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.50	0.55	0.60
A1	0.00	-	0.05
A3	0.15 REF		
D	0.95	1.00	1.05
E	0.95	1.00	1.05
b	0.10	0.15	0.20
L	0.30	0.40	0.50
e	0.35 BSC		

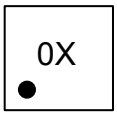
Recommend pad outline



Tape Information



Marking Information



Note：X (Tracking Number) is variable, according to the wafer lot number.

Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
0.0	2025-04-23	Preliminary Version	Wanggp	Wanggp	Liujy
0.1	2025-05-07	Update Typeset	Chenzx	Wanggp	Liujy
0.2	2025-05-14	Add RT pin description	Yin Peng	Wanggp	Liujy
1.0	2025-09-17	Official Version	Licx	Wanggp	Liujy
1.1	2026-02-02	Update T_{DL} range 0.1s to 20s.	Yin Peng	Wanggp	Liujy