ET5H1XX - High Input Very-Low IQ 150mA LDO

General Description

ET5H1XX series are the high input very low IQ 150mA LDO that operates from 1.8V~5V, is designed specifically for portable battery-powered applications which require ultra-low quiescent current. The very-low consumption of type 1.5uA ensures long battery life and dynamic transient boost feature improves device transient response for wireless communication applications.

ET5H1XX series are offered SOT89-3, SOT23-5, SOT23-3, SSOT23-3, DNF4(1mm × 1mm) packages

Features

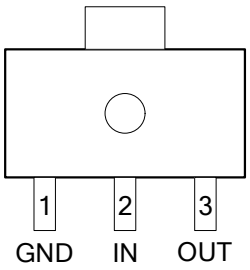
- Wide Input Voltage Range from 3V to 30V
- Up to 150mA Load Current
- Very Low I_Q is 1.5μA Typical
- Fixed Output Voltage are 1.8V, 2.5V, 3.0V, 3.3V, 3.6V, 4.4V, 5.0V, etc.
- Low Dropout is 720mV at 150mA Load @V_{OUT}=5.0V
- Low Dropout is 820mV at 150mA Load @V_{OUT}=3.3V
- Short Current Protection is 100mA
- Excellent Load/Line Transient Response
- Part No. and Package Information

Part No.	Package	Packing Option	MSL
ET5H1XXB	SOT89-3	Tape and Reel, 1K/Reel	3
ET5H1XXF		Tape and Reel, 1K/Reel	3
ET5H1XXS	SOT23-3	Tape and Reel, 3K/Reel	3
ET5H1XXS1	SSOT23-3	Tape and Reel, 3K/Reel	3
ET5H1XXT	SOT23-5	Tape and Reel, 3K/Reel	3
ET5H1XX	SOT23-5 (Default)	Tape and Reel, 3K/Reel	3
ET5H1XXY	DFN4(1mm × 1mm)	Tape and Reel, 10K/Reel	1

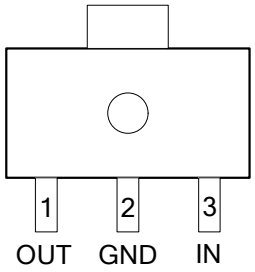
Mark Specification Label

Part No.	Marking							V_{OUT}
	SOT89-3		SOT23-3	SSOT23-3	DFN4	SOT23-5		
	XXB	XXF	XXS	XXS1	XXY	XXT	XX	
ET5H118	18B	18F	18S	18S1	CX	18T	18	1.8V
ET5H125	25B	25F	25S	25S1	FX	25T	25	2.5V
ET5H130	30B	30F	30S	30S1	GX	30T	30	3.0V
ET5H133	33B	33F	33S	33S1	EX	33T	33	3.3V
ET5H136	36B	36F	36S	36S1	OX	36T	36	3.6V
ET5H144	44B	44F	44S	44S1	/	44T	44	4.4V
ET5H150	50B	50F	50S	50S1	5X	50T	50	5.0V

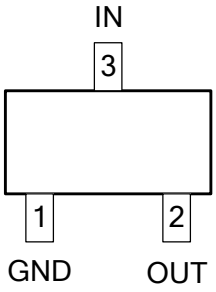
Pin Configuration



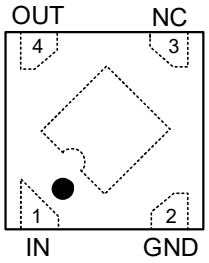
ET5H1XXB(SOT89-3)



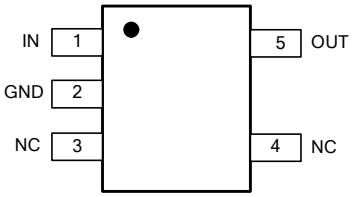
ET5H1XXF(SOT89-3)



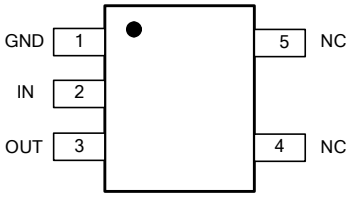
ET5H1XXS/S1(SOT23-3/SSOT23-3)



ET5H1XXY(DFN4)



ET5H1XXT(SOT23-5)



ET5H1XX(SOT23-5)

TOP VIEW

Figure 1. Pin Configuration

Pin Function

Pin No.						Pin Name	Pin Function
SOT89-3		SOT23-3 SSOT23-3	DFN4	SOT23-5			
XXB	XXF	XXS/S1	XXY	XXT	XX		
1	2	1	2	2	1	GND	Ground.
2	3	3	1	1	2	IN	Supply Input Pin.
3	1	2	4	5	3	OUT	Output Pin.
			3	3,4	4,5	NC	No Connection.

Block Diagram

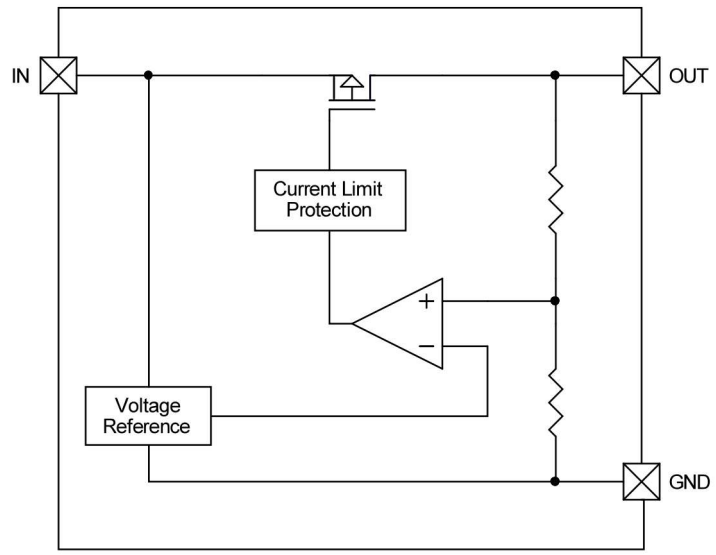


Figure 2. Block Diagram

Functional Description

Input Capacitor

A 1μF~10μF ceramic capacitor is recommended to connect between IN and GND pins to decouple input power supply glitch and noise. The amount of the capacitance may be increased without limit. This input capacitor must be located as close as possible to the device to assure input stability and less noise. For PCB layout, a wide copper trace is required for both IN and GND.

Output Capacitor

An output capacitor is required for the stability of the LDO. The recommended output capacitance is from 1μF to 10μF, Equivalent Series Resistance (ESR) is from 5mΩ to 100mΩ, and temperature characteristics are X7R or X5R. Higher capacitance values help to improve load/line transient response. The output capacitance may be increased to keep low undershoot/overshoot. Place output capacitor as close as possible to OUT and GND pins.

Dropout Voltage

The ET5H1XX uses a PMOS pass transistor to achieve low dropout. When (V_{IN} – V_{OUT}) is less than the dropout voltage (V_{DO}), the PMOS pass device is in the linear region of operation and the input-to-output resistance is the $R_{DS(ON)}$ of the PMOS pass element. V_{DO} scales approximately with output current because the PMOS device behaves like a resistor in dropout mode. As with any linear regulator, PSRR and transient response degrade as (V_{IN} – V_{OUT}) approaches dropout operation.

Low Quiescent Current

The ET5H1XX consuming only around 1.5μA for all input range and output loading, provides great power saving in portable and low power applications.

Short Current Limit Protection

When output current at the OUT pin is higher than current limit threshold or the OUT pin is short-circuit to GND, the short current limit protection will be triggered and clamp the output current to approximately 100mA to prevent over-current and to protect the regulator from damage due to overheating.

Layout Guidelines

- Place input and output capacitors as close to the device as possible.
- Use copper planes for device connections in order to optimize thermal performance.
- Place thermal vias around the device to distribute heat.
- Do not place a thermal via directly beneath the thermal pad. A via can wick solder or solder paste away from the thermal pad joint during the soldering process, leading to a compromised solder joint on the thermal pad.

Absolute Maximum Ratings

Symbol	Rating	Value	Unit
V_{IN}	Input Voltage (1)	-0.3~36	V
V_{OUT}	Output Voltage	-0.3~6.0	V
T_J	Maximum Junction Temperature	-40~150	°C
T_{STG}	Storage Temperature	-65~150	°C
T_L	Lead Temperature (Soldering, 10 sec)	260	°C
V_{ESD}	Human Body Model Capability (2)	±2000	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Note1: Refer to electrical characteristics and application information for safe operating area.

Note2: This device series incorporates ESD protection and is tested by the following methods:

ESD Human Body Model tested per JEDEC JS-001

Thermal Characteristics

Symbol	Package	Ratings	Value	Unit
$R_{\theta JA}$	SOT89-3	Thermal Characteristics, Thermal Resistance, Junction-to-Air	135	°C/W
	SOT23-5		250	
	SOT23-3		360	
	SSOT23-3		380	
	DFN4		250	
P_D	SOT89-3	Power Dissipation@25°C PCB Board Dimension: 40mm x 40mm (2layer) Copper :1OZ	750	mW
	SOT23-5		400	
	SOT23-3		280	
	SSOT23-3		260	
	DFN4		400	

Recommended Operating Conditions

Symbol	Items	Rating	Unit
V_{IN}	Input Voltage	3.0 to 30	V
I_{OUT}	Output Current	0 to 150	mA
T_J	Operating Junction Temperature	-40 to 125	°C
C_{IN}	Effective Input Ceramic Capacitor Value	1 to 10	uF
C_{OUT}	Effective Output Ceramic Capacitor Value	1 to 10	uF
ESR	Input and Output Capacitor Equivalent Series Resistance (ESR)	5 to 100	mΩ

Electrical Characteristics

(V_{IN} = V_{OUT} +2V, I_{OUT} = 1mA, C_{IN} = C_{OUT} = 1.0μF, unless otherwise noted. Typical values are at T_A = +25°C.)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{IN}	Operating Input Voltage[3]		3.0		30	V
V_{OUT}	Output Voltage	I_{OUT} = 1mA, 25°C	-2		+2	%
		I_{OUT} = 1mA, -40~125°C	-3		+3	
I_Q	Quiescent Current	I_{OUT} = 0mA, 25°C		1.5	3.0	μA
$Line_{REG}$	Line Regulation[6]	V_{IN} = V_{OUT} + 2V to 30V, I_{OUT} = 10mA (ΔV_{OUT} / ΔV_{IN} / V_{OUT})		0.01	0.04	%/V
$Load_{REG}$	Load Regulation[6]	1mA ≤ I_{OUT} ≤ 150mA, V_{IN} = V_{OUT} + 2V		5	25	mV
V_{DROP}	Dropout Voltage I_{OUT}=150mA[4] [6]	V_{OUT} = 1.8V		1300	1450	mV
		V_{OUT} = 2.5V		1200	1350	
		V_{OUT} = 3.0V		840	960	
		V_{OUT} = 3.3V		820	950	
		V_{OUT} = 3.6V		800	930	
		V_{OUT} = 4.4V		790	920	
		V_{OUT} = 5V		720	900	
V_{DROP}	Dropout Voltage I_{OUT}=100mA[4] [6]	V_{OUT} = 1.8V		880	1050	mV
		V_{OUT} = 2.5V		800	1000	
		V_{OUT} = 3.0V		530	700	
		V_{OUT} = 3.3V		520	680	
		V_{OUT} = 3.6V		500	660	
		V_{OUT} = 4.4V		490	630	
		V_{OUT} = 5V		420	600	
I_{LIMT}	Current Limit	V_{IN} = V_{OUT} + 2V, R_{OUT} = 1Ω	200			mA
I_{SHORT}	Short Current Protection	OUT Short to GND		100		mA
e_N	Output Noise Voltage[5]	V_{IN} = V_{OUT} + 2V, I_{OUT} = 1mA, f = 10Hz to 100KHz, V_{OUT} = 3V, C_{OUT} = 1μF		90		μVrms

Note3: Here V_{IN} means internal circuit can work normal. If V_{IN} < V_{OUT}, Output voltage follows V_{IN}(I_{OUT} = 1mA), circuit is safety.

Note4: V_{DROP} FT test method: test the V_{OUT} voltage at V_{SET} + $V_{DROPMAX}$ with 150mA output current.

Note5: Guaranteed by design and characterization. not a FT item.

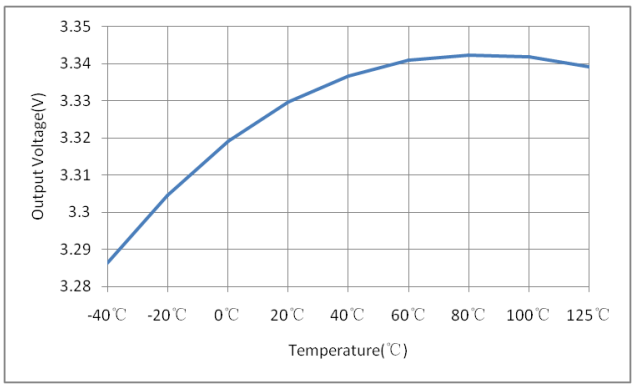
Note6: At high temperatures, the maximum load current can be calculated according to the following formula:

$$I_{OUT_MAX} = (T_J - T_A) / R_{\theta JA} / (V_{IN} - V_{OUT})$$

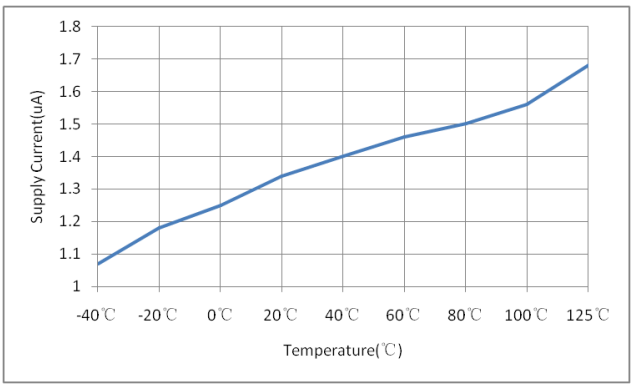
Typical Characteristics

VOLTAGE VERSION 3.3V

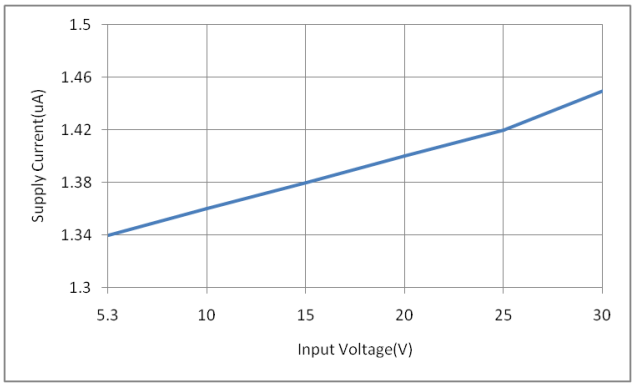
(V_{IN} = 5.3V; I_{OUT} = 1mA, C_{IN} = C_{OUT} = 1.0μF, unless otherwise noted. Typical values are at T_A = +25℃.)



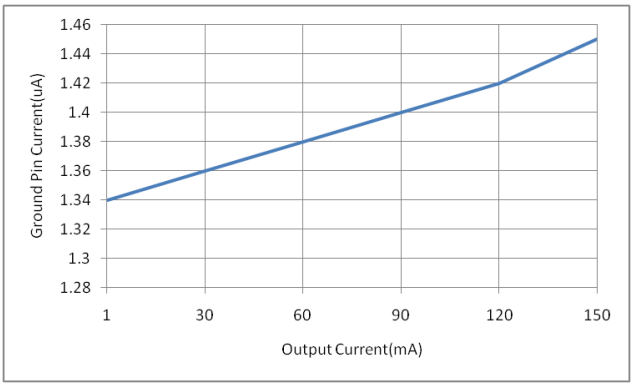
Output Voltage VS Temperature



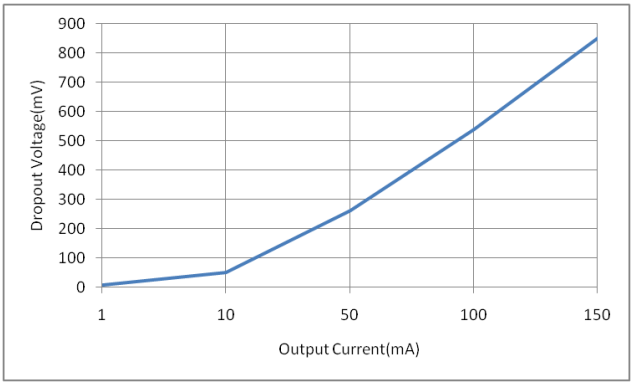
Supply Current VS Temperature



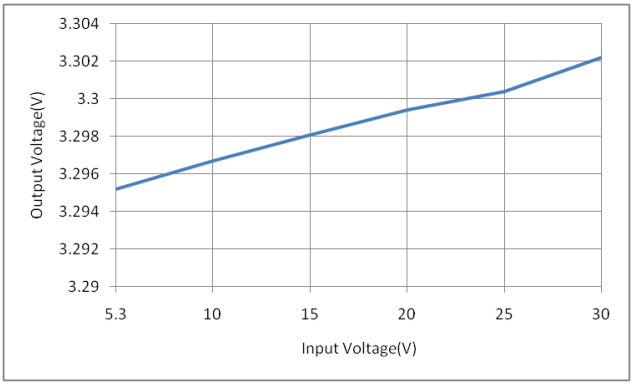
Supply Current VS Input Voltage



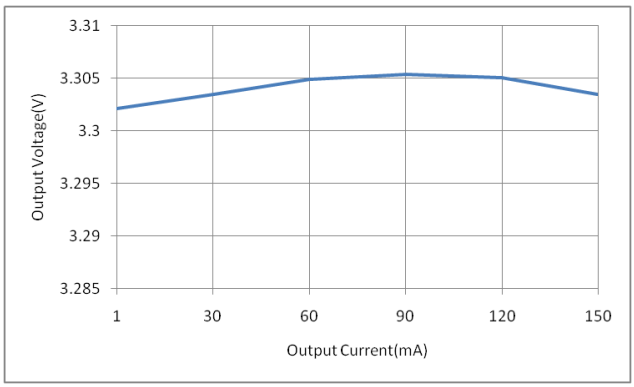
Ground Pin Current VS Output Current



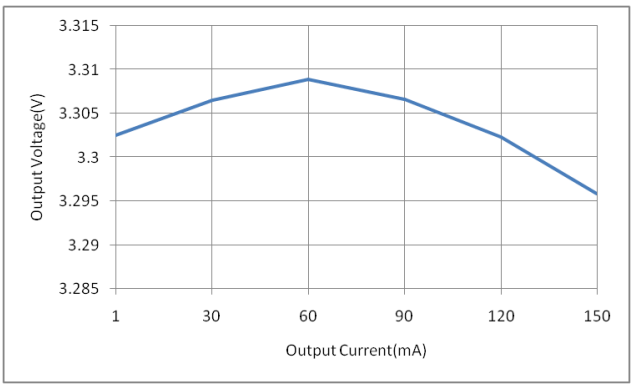
Dropout Voltage VS Output Current



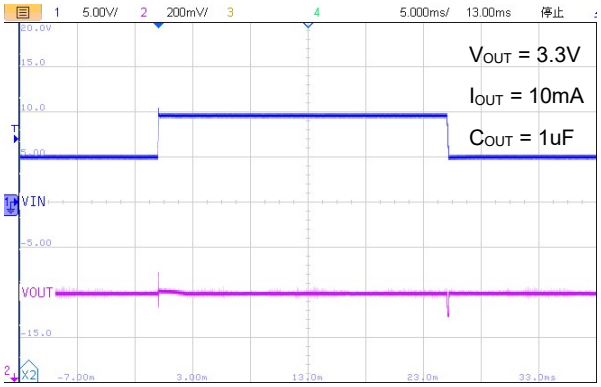
Output Voltage VS Input Voltage



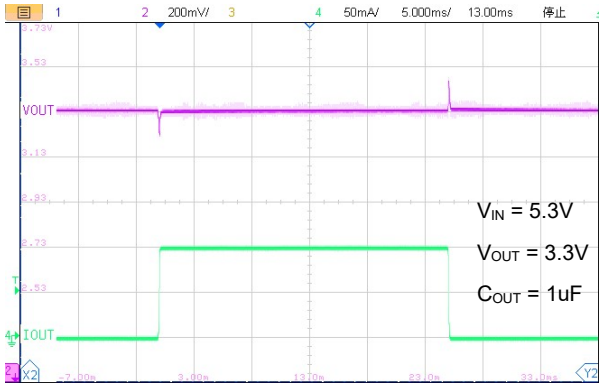
Output Voltage VS Output Current(V_{IN} = 5.3V)



Output Voltage VS Output Current(V_{IN} = 8V)



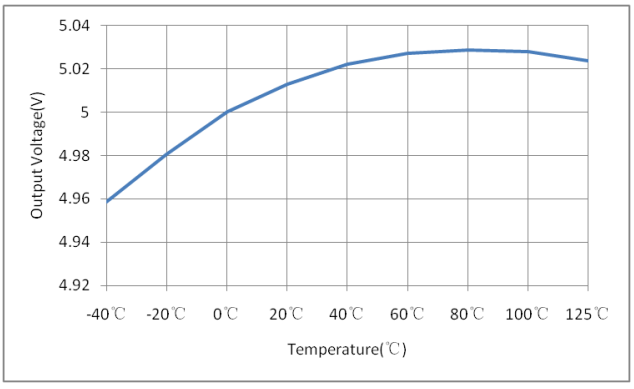
Line Transient Response



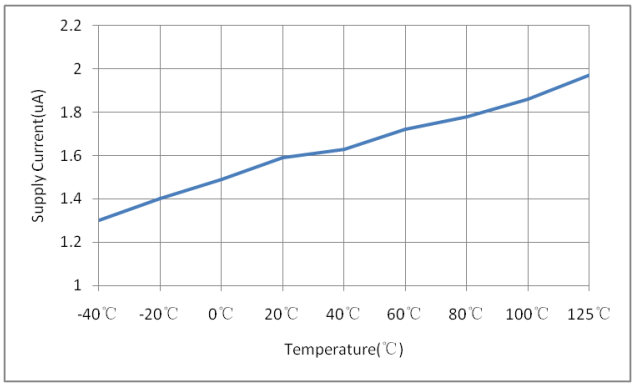
Load Transient Response

VOLTAGE VERSION 5.0V

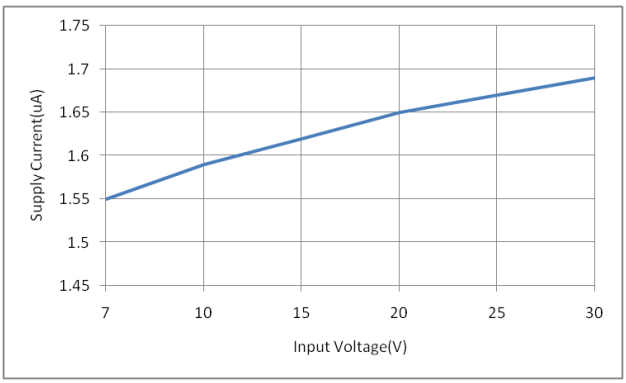
(V_{IN} = 7V; I_{OUT} = 1mA, C_{IN} = C_{OUT} = 1.0μF, unless otherwise noted. Typical values are at T_A = +25℃.)



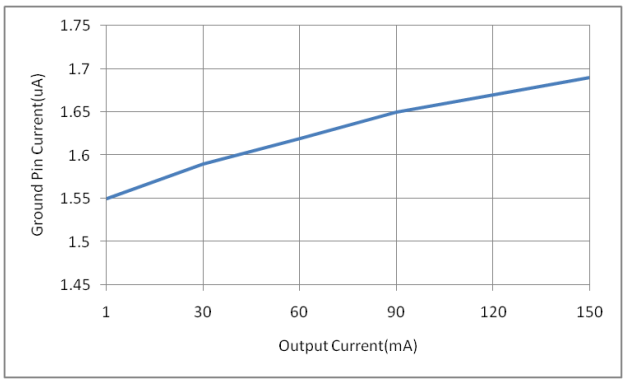
Output Voltage VS Temperature



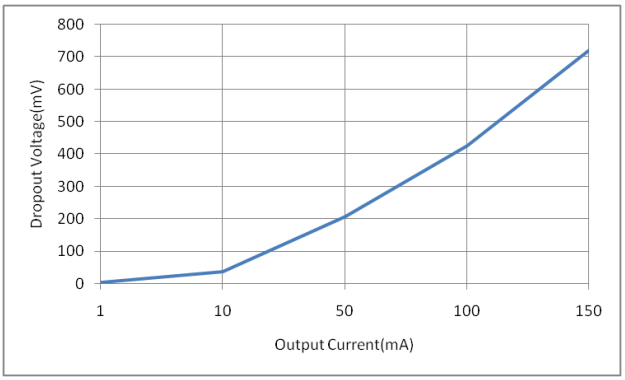
Supply Current VS Temperature



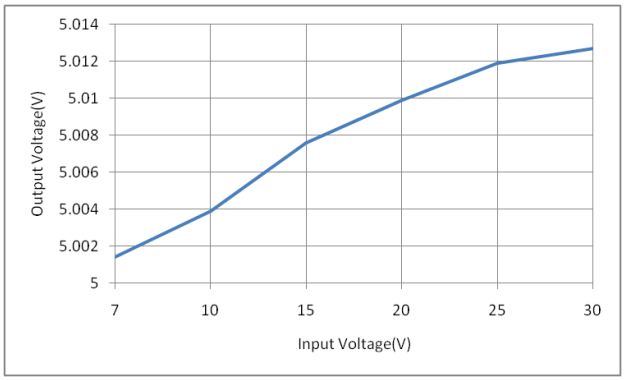
Supply Current VS Input Voltage



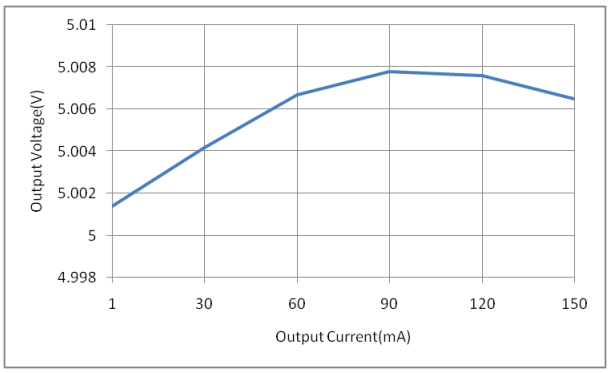
Ground Pin Current VS Output Current



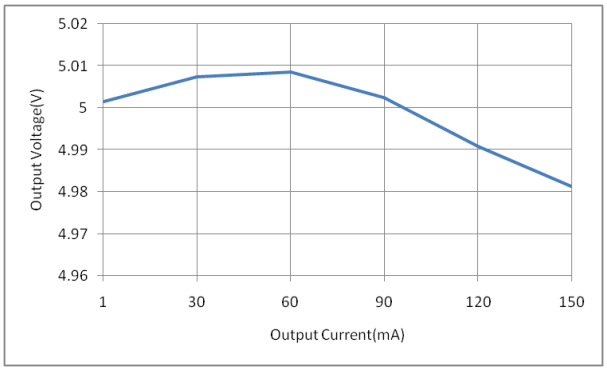
Dropout Voltage VS Output Current



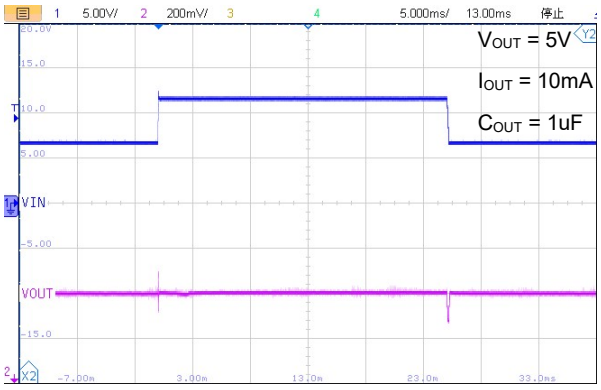
Output Voltage VS Input Voltage



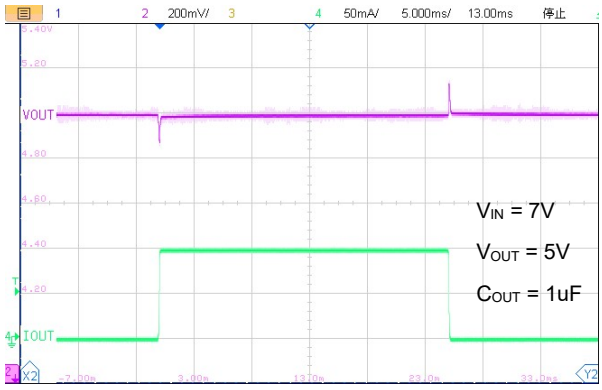
Output Voltage VS Output Current(V_{IN} = 7V)



Output Voltage VS Output Current(V_{IN} = 10V)



Line Transient Response



Load Transient Response

Application Circuits

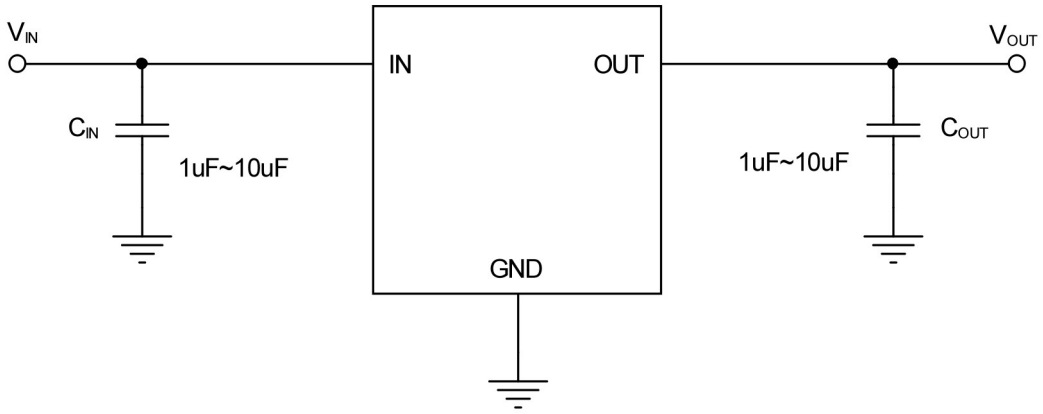
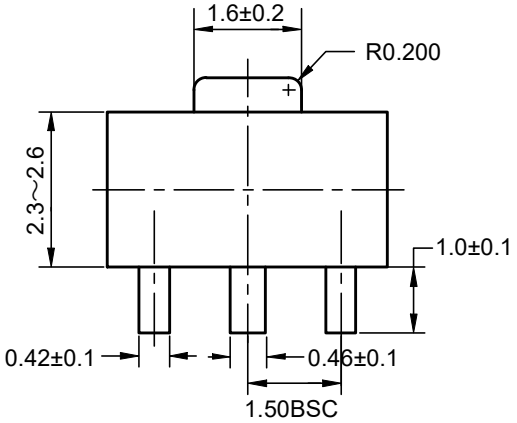


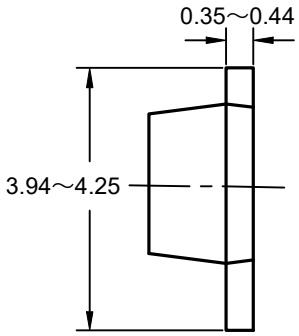
Figure 3. Application Circuits

Package Dimension

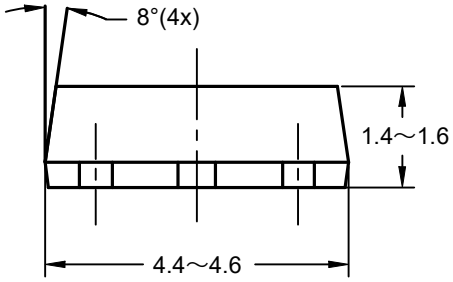
SOT89-3



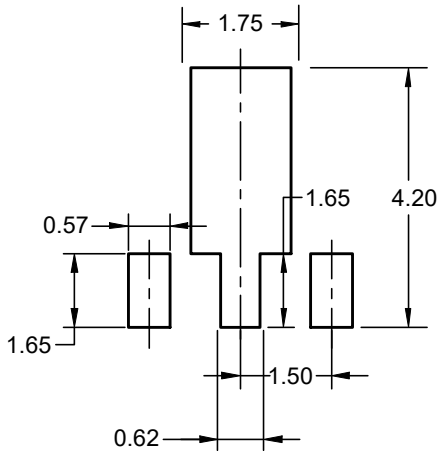
Top View



Side View



Side View

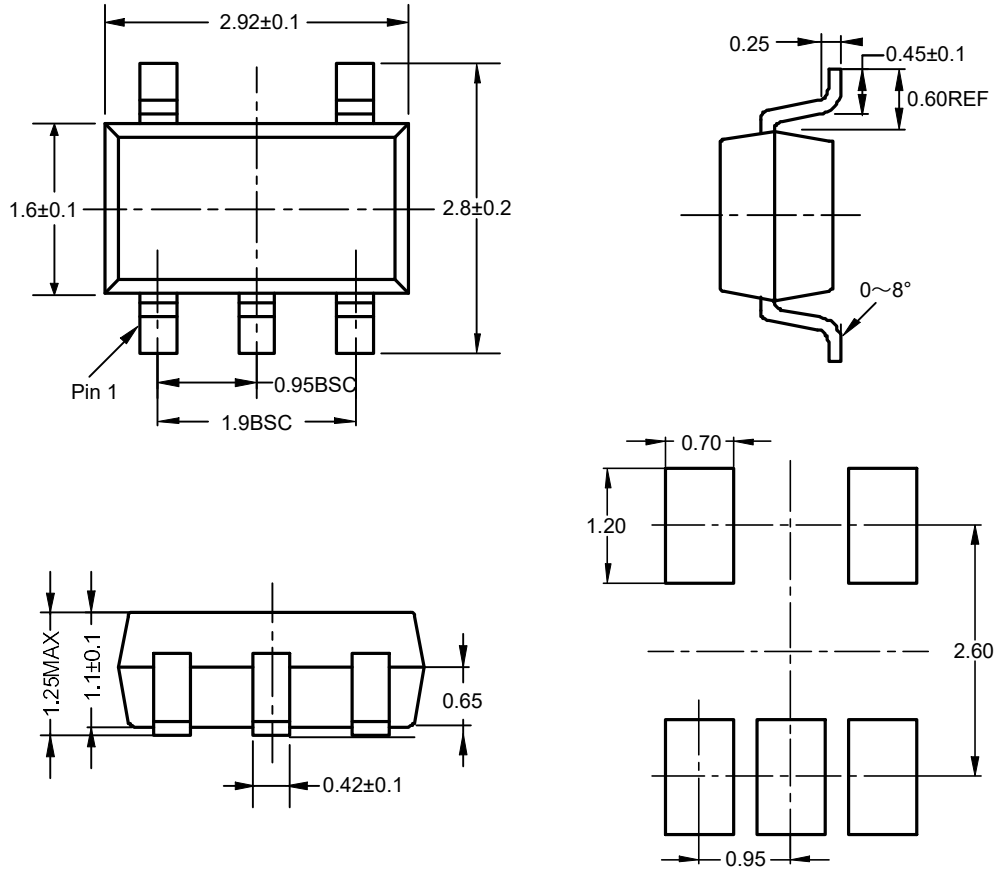


Recommended Land Pattern

Unit: mm

Figure 4. SOT89-3 Package Dimension

SOT23-5

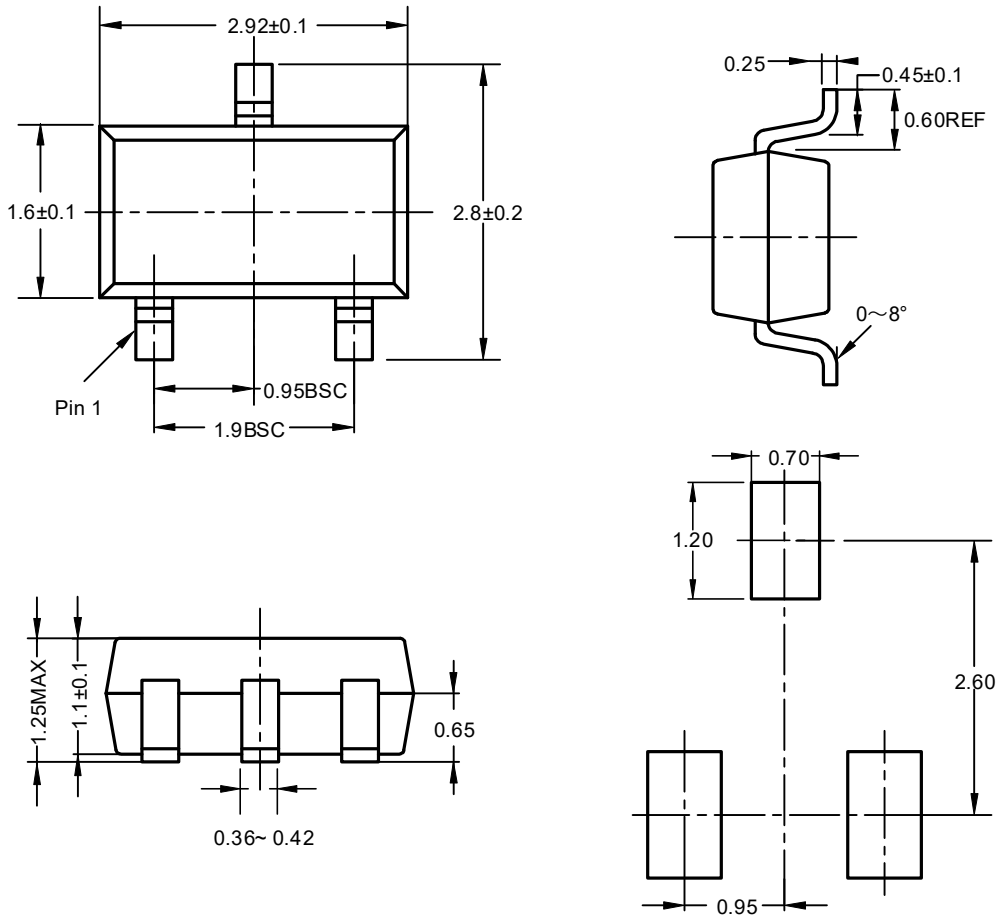


Recommended Land Pattern

Unit: mm

Figure 5. SOT23-5 Package Dimension

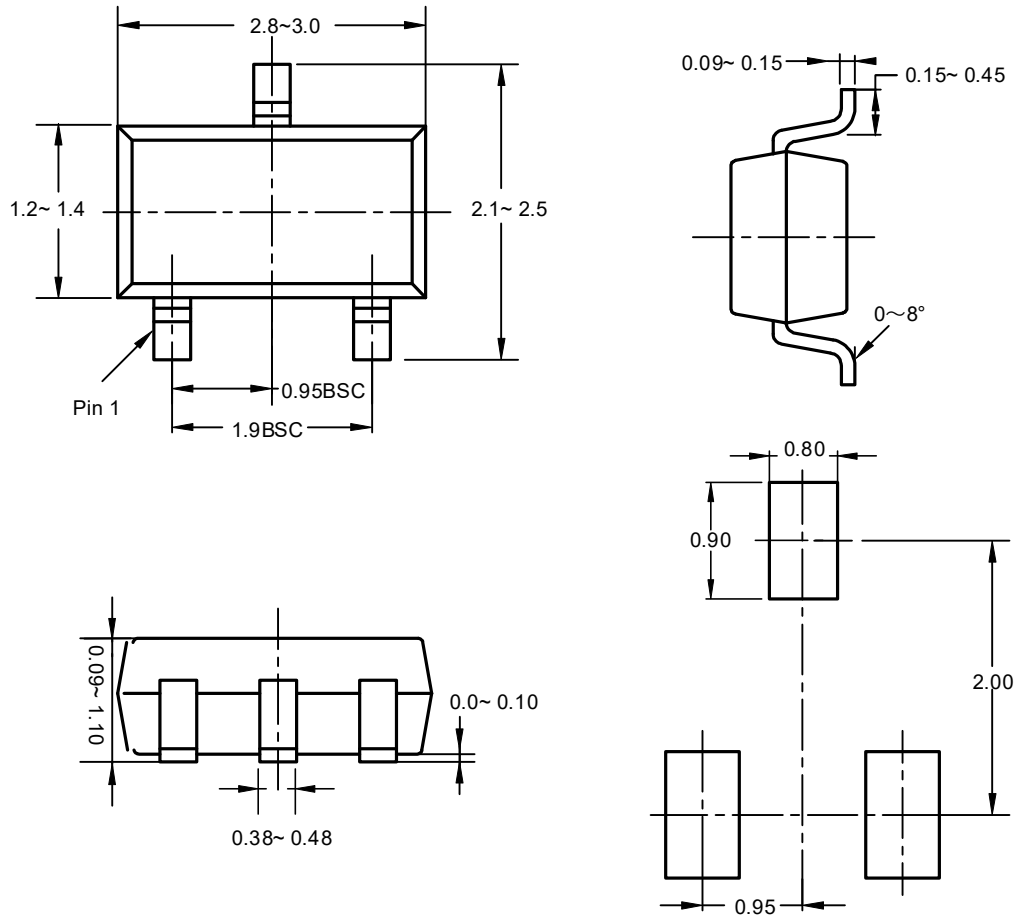
SOT23-3



Unit: mm

Figure 6. SOT23-3 Package Dimension

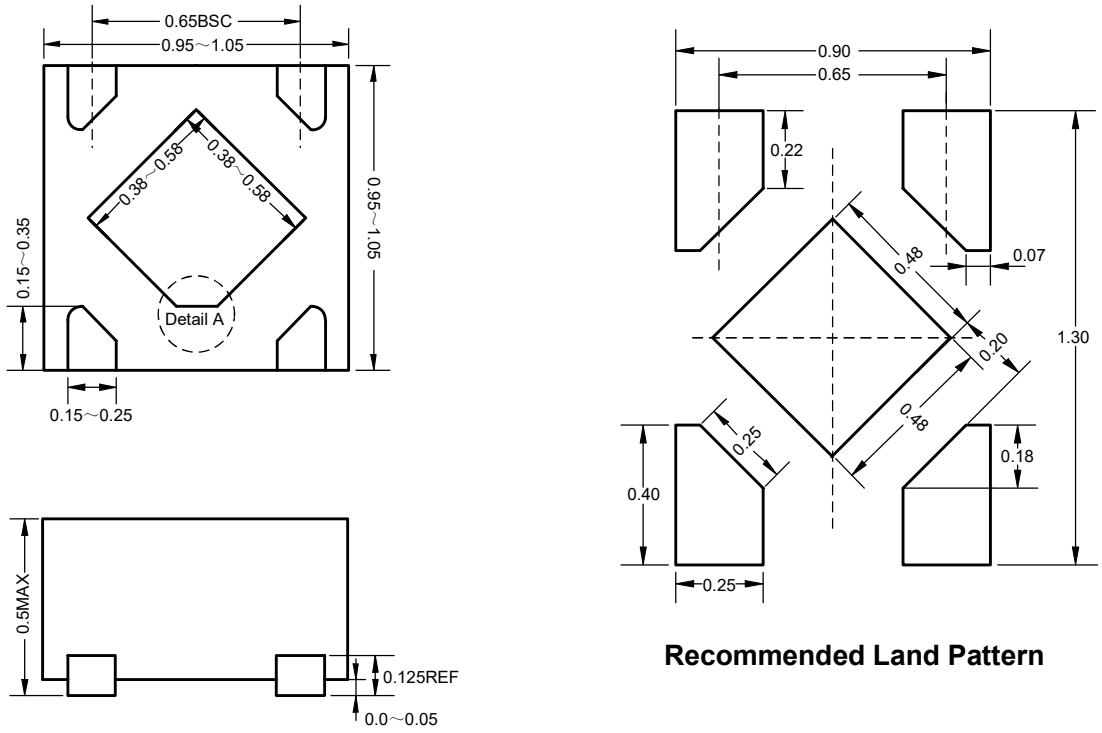
SSOT23-3



Unit: mm

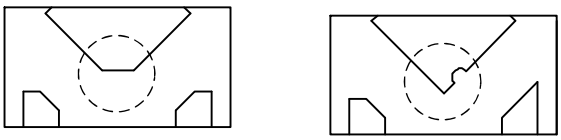
Figure 7. SSOT23-3 Package Dimension

DFN4(1mm × 1mm)



Recommended Land Pattern

Detail A: (PIN1 shape)



Unit: mm

Figure 8. DFN4 Package Dimension

Tape Information

SOT89-3

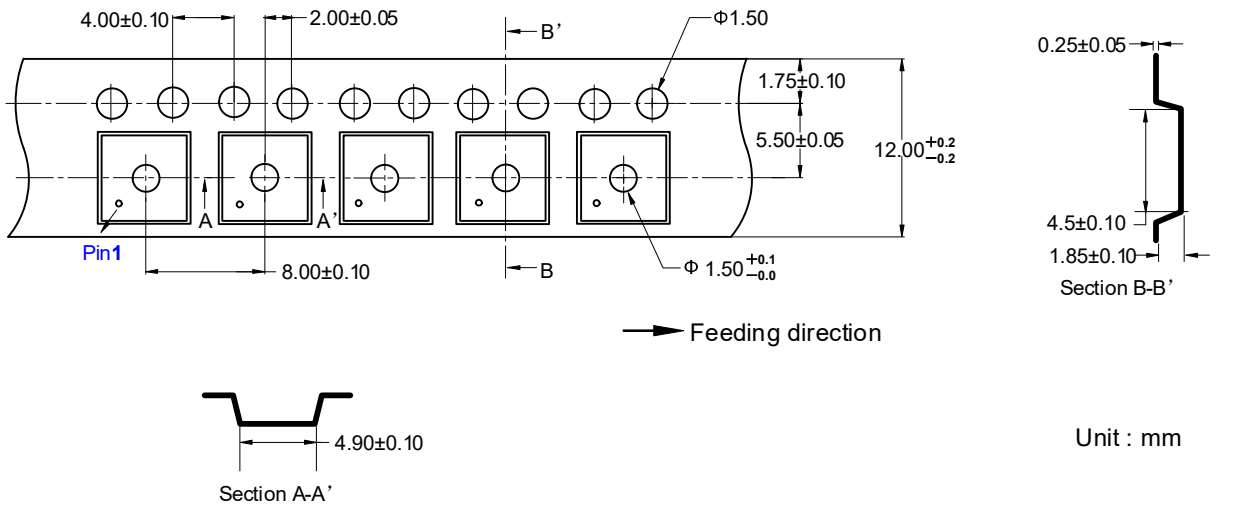


Figure 9. SOT89-3 Tape Information

SOT23-3

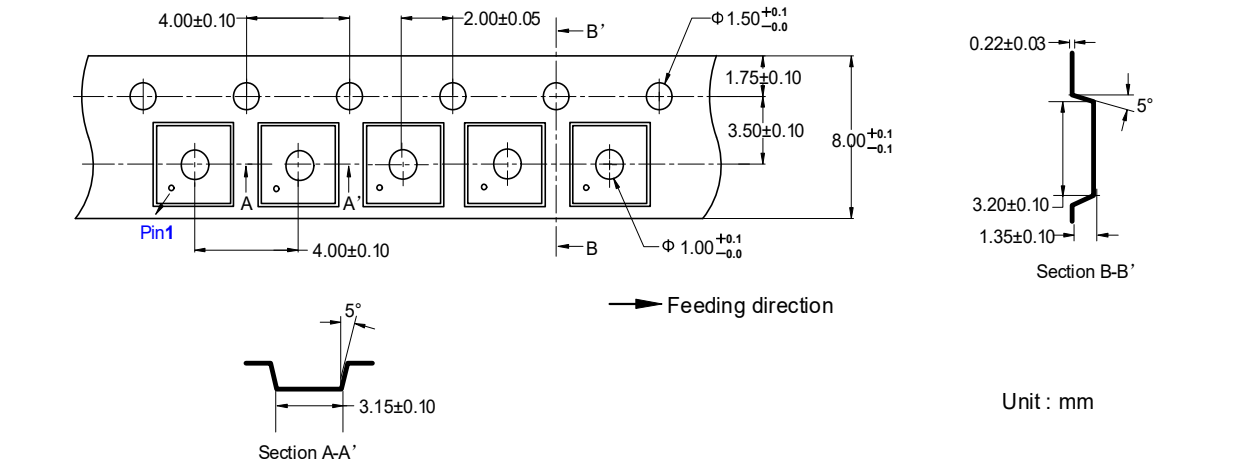


Figure 10. SOT23-3 Tape Information

SSOT23-3

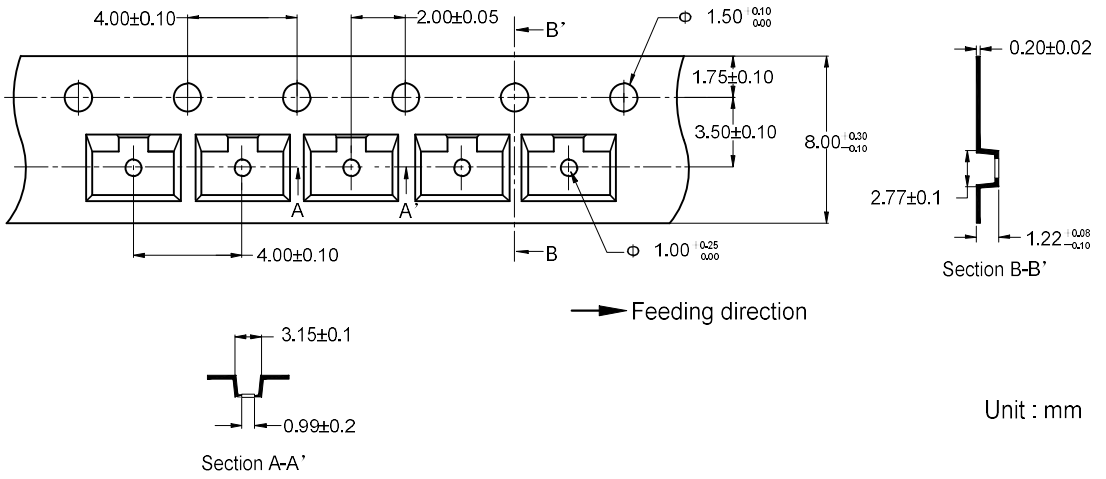


Figure 11. SSOT23-3 Tape Information

SOT23-5

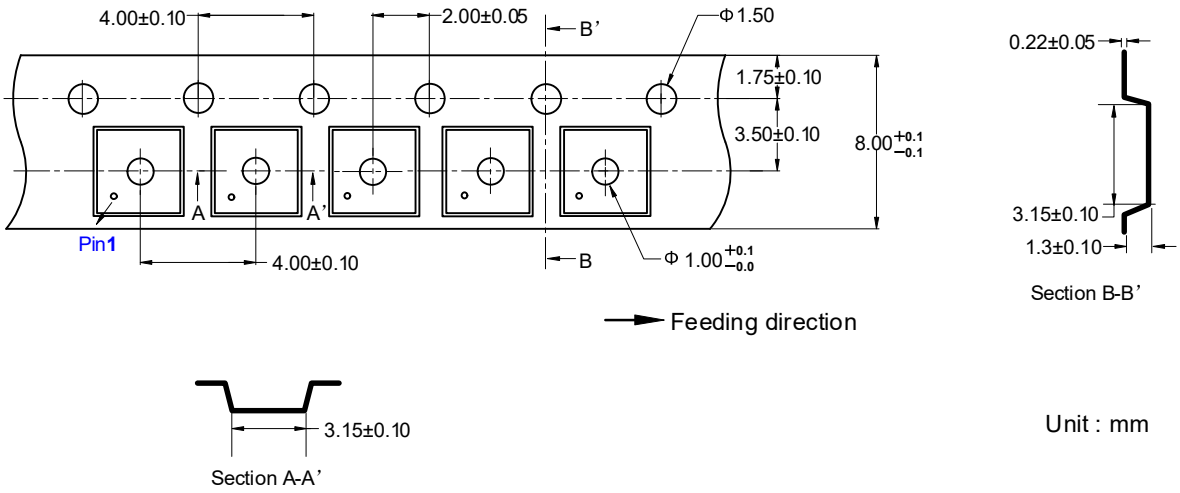


Figure 12. SOT23-5 Tape Information

DFN4

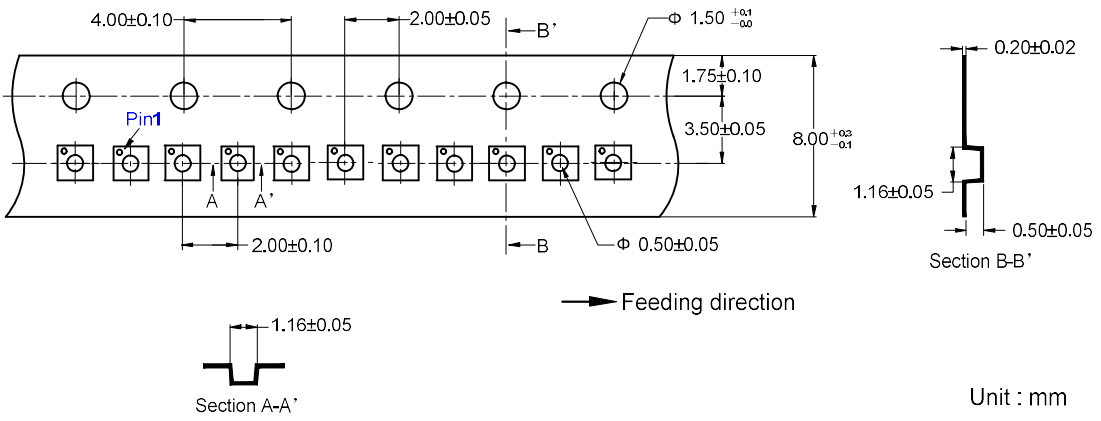
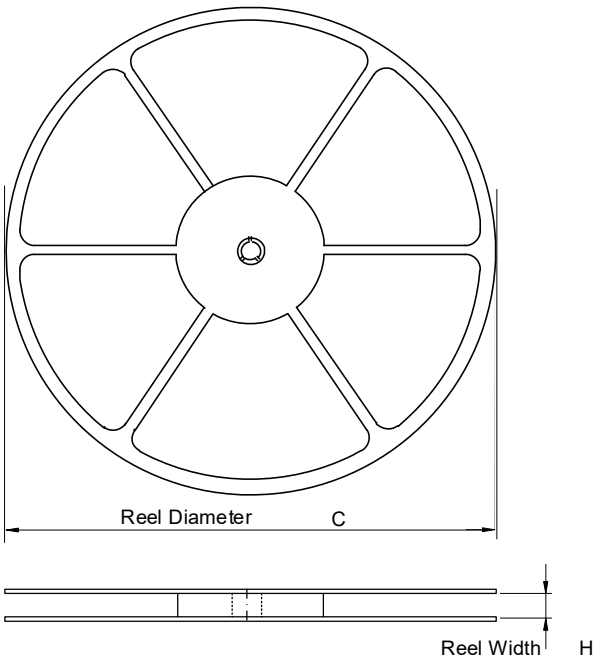


Figure 13. DFN4 Tape Information

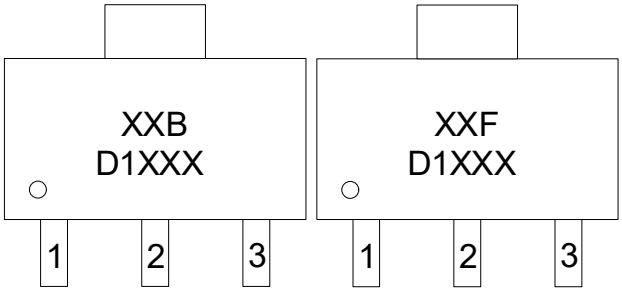
Reel Information



Package	Diameter C	Width H
SOT89-3	178mm	12.5mm
SOT23-5	178mm	8.4mm
SOT23-3	178mm	8.4mm
SSOT23-3	178mm	8.4mm
DFN4	178mm	8.4mm

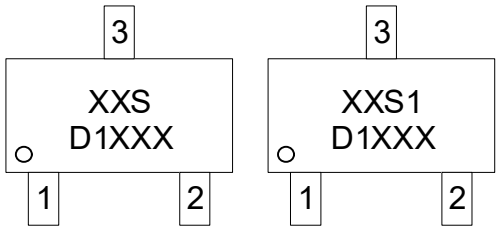
Marking Information

SOT89-3



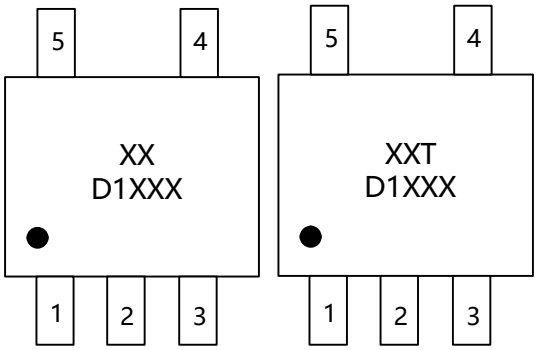
XXB / XXF - Part Number (ET5H1XXB / ET5H1XXF)
D1XXX - Tracking Number

SOT23-3/SSOT23-3



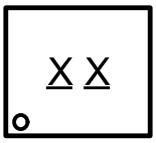
XXS / XXS1 - Part Number (ET5H1XXS / ET5H1XXS1)
D1XXX - Tracking Number

SOT23-5



XX / XXT - Part Number (ET5H1XX / ET5H1XXT)
D1XXX - Tracking Number

DFN4



X - V_{OUT} Version
X - Tracking Number

Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2018-08-20	Original Version	Liuxm	Liuxm	Zhujl
1.1	2018-10-11	Add DFN4 Package	Liuxm	Liuxm	Zhujl
1.2	2018-11-26	Add SOT23-5	Liuxm	Liuxm	Liujy
1.3	2018-12-27	Change Size of SOT23-3&SOT23-5	Liuxm	Liuxm	Liujy
1.4	2019-01-25	Revise Dropout Voltage	Shib	Shib	Liujy
1.5	2019-01-30	Add Vout in AMR	Liuxm	Liuxm	Liujy
1.6	2019-02-01	Update ESD Result	Wuxj	Wuxj	Liujy
1.7	2019-02-26	Revise Reg_{LINE}& Reg_{LOAD}	Shib	Shib	Liujy
1.8	2019-06-18	Add Vout=3.6V	Liuxm	Liuxm	Liujy
1.9	2019-10-09	Update Package Size	Liuxm	Liuxm	Liujy
2.0	2021-08-02	Update Marking Information	Liuxm	Liuxm	Liujy
2.1	2023-05-10	Update Typeset	Shibo	Liuxm	Zhujl
2.2	2023-06-05	Update Mmarking	Shibo	Liuxm	Liujy
2.3	2023-10-07	Update Package	Shibo	Liuxm	Liujy
2.4	2025-02-27	Update Package	Shibo	Liuxm	Liujy
2.5	2025-08-07	Add SSOT23-3 Package	Shibo	Liuxm	Liujy
2.6	2025-10-14	Update T_J Range	Yangxiaoxu	Liuxm	Liujy
2.7	2026-01-12	Add Marking	Yangxiaoxu	Liuxm	Liujy
2.8	2026-09-10	Add 4.4V Version	Yangxiaoxu	Liuxm	Liujy