Single Bus Buffer Gate with 3-State Output

General Description

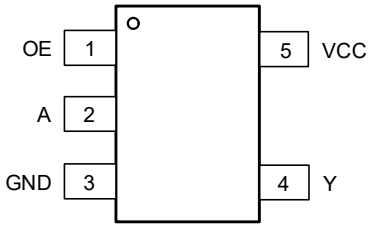
The ET74LVC1G126 device is a single bus buffer gate/line driver with 3-state output. The output is disabled when the output-enable (OE) input is low. When OE is high, data is passed from the A input to the Y output.

Features

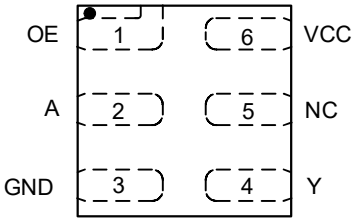
- Designed for 1.65V to 5.5V V_{CC} Operation
- Over-voltage Tolerant Inputs Accept Voltages to 5.5V
- ±32mA Balanced Output Sink and Source Capability
- Near Zero Static Supply Current Substantially Reduces System Power Requirements
- These Devices are Pb-Free and RoHS Compliant
- 3-State OE Input is Active-High
- ESD Protection Complies with JESD22 Standard
  -- HBM: ±4000V Pass (JEDEC JS-001)
  -- CDM: ±1000V Pass (JEDEC JS-002)
- Latch-up Performance Exceeds ±100mA per JEDEC JESD78F
- Part No. and Package Information

Part No.	Package	Packing Option	MSL
ET74LVC1G126	SC70-5 (1.3mm × 2.1mm)	Tape and Reel, 3K/Reel	1
ET74LVC1G126T	SOT23-5 (1.6mm × 2.9mm)	Tape and Reel, 3K/Reel	3
ET74LVC1G126N	DFN6 (1.0mm × 1.0mm × 0.37mm)	Tape and Reel, 5K/Reel	1
ET74LVC1G126N1	DFN6 (1.0mm × 1.0mm × 0.55mm)	Tape and Reel, 5K/Reel	1
ET74LVC1G126Y1	DFN4 (0.8mm × 0.8mm)	Tape and Reel, 10K/Reel	1

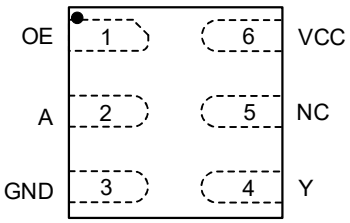
Pin Configuration



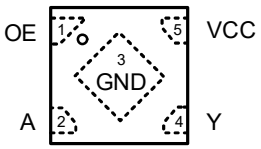
SC70-5 / SOT23-5



DFN6 (ET74LVC1G126N)



DFN6 (ET74LVC1G126N1)



DFN4

Figure1. Pin Configuration

Pin Function

SC70-5/ SOT23-5

Pin No.	Pin Name	Function
1	OE	Enable Input
2	A	Input
3	GND	Ground
4	Y	Output
5	VCC	Supply Voltage

DFN6

Pin No.	Pin Name	Function
1	OE	Enable Input
2	A	Input
3	GND	Ground
4	Y	Output
5	NC	No Connect
6	VCC	Supply Voltage

DFN4

Pin No.	Pin Name	Function
1	OE	Enable Input
2	A	Input
3	GND	Ground
4	Y	Output
5	VCC	Supply Voltage

Block Diagram

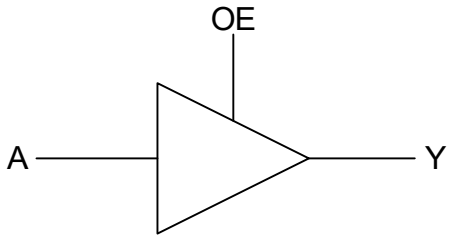


Figure2. Logic Symbol

Functional Table

Input		Output
OE	**A**	**Y**
H	L	L
H	H	H
L	X	Z

Absolute Maximum Ratings

Symbol	Parameter		Value	Unit
V_{CC}	DC Supply Voltage (VCC Pin)		-0.5 to 6.5	V
V_I	DC Input Voltage(1)		-0.5 ≤ V_I ≤ 6.5	V
V_O	DC Output Voltage Output in Higher or Low State		-0.5 to V_{CC} + 0.5	V
I_{IK}	DC Input Diode Current, V_I < GND		-50	mA
I_{OK}	DC Output Diode Current, V_O < GND, V_O > V_{CC}		±50	mA
I_O	DC Output Sink Current		±50	mA
I_{CC}	DC Supply Current per Supply Pin		100	mA
I_{GND}	DC Ground Current per Supply Pin		-100	mA
T_{STG}	Storage Temperature Range		-65 to 150	°C
T_L	Lead Temperature, Soldering 10 Seconds		260	°C
T_J	Max Junction Temperature		150	°C
V_{ESD}	ESD Classification	Human Body Model (2)	±4000	V
		Charged Device Model (3)	±1000	
I_{LU}	Max Latch up Current Above V_{CC} and GND at 125°C (4)		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Note1: I_O absolute maximum rating must be observed.

Note2: HBM tested per JEDEC JS-001;

Note3: CDM tested per JEDEC JS-002;

Note4: Latch up Current Maximum Rating tested per JEDEC JESD78F.

Thermal Characteristics

Symbol	Package	Ratings	Value	Unit
$R_{\theta JA}$	SC70-5	Thermal Characteristics, Thermal Resistance, Junction-to-Air	300	°C/W
	SOT23-5		250	
	DFN6		440	
	DFN4		285	
P_D	SC70-5	Power Dissipation in Still Air at 85°C	215	mW
	SOT23-5		260	
	DFN6		150	
	DFN4		350	

Recommended Operating Conditions

Symbol	Parameter		Min	Max	Unit
V_{CC}	DC Supply Voltage Operating		1.65	5.5	V
	Date Retention Voltage Operating		1.5	5.5	
V_I	DC Input Voltage		0	V_{CC}	V
V_O	DC Output Voltage (High or Low State)		0	V_{CC}	V
T_A	Operating Temperature Range		-40	125	°C
t_r, t_f	Input Rise and Fall Time	V_{CC} = 2.5V ± 0.2V	0	20	ns/V
		V_{CC} = 3.0V ± 0.3V	0	10	
		V_{CC} = 5.0V ± 0.5V	0	5	

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied.

Electrical Characteristics

DC Electrical Characteristics

Symbol	Parameter	Condition	V_{CC}(V)	T_A = 25°C			-40°C ≤ T_A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
V_{IH}	High-Level Input Voltage		1.65~1.95 2.3~5.5	0.75V_{CC} 0.7V_{CC}			0.75V_{CC} 0.7V_{CC}		V
V_{IL}	Low-Level Input Voltage		1.65~1.95 2.3~5.5			0.25V_{CC} 0.3V_{CC}		0.25V_{CC} 0.3V_{CC}	V
V_{OH}	High-Level Output Voltage	I_{OH} = -100uA	1.65~5.5	V_{CC} - 0.1	V_{CC}		V_{CC} - 0.1		V
		I_{OH} = -3mA	1.65	1.29	1.52		1.29		
		I_{OH} = -8mA	2.3	1.9	2.1		1.9		
		I_{OH} = -12mA	2.7	2.2	2.4		2.2		
		I_{OH} = -24mA	3.0	2.3	2.5		2.3		
		I_{OH} = -32mA	4.5	3.8	4.0		3.8		
V_{OL}	Low-Level Output Voltage	I_{OH} = 100uA	1.65~5.5		0.0	0.1		0.1	V
		I_{OL} = 3mA	1.65		0.08	0.24		0.24	
		I_{OL} = 8mA	2.3		0.20	0.3		0.3	
		I_{OL} = 12mA	2.7		0.22	0.4		0.4	
		I_{OL} = 24mA	3.0		0.38	0.55		0.55	
		I_{OL} = 32mA	4.5		0.42	0.55		0.55	
I_{IN}	Input Leakage Current	V_I = 5.5V or GND	0~5.5		±0.1			±1.0	uA

DC Electrical Characteristics (Continued)

Symbol	Parameter	Condition	V_{CC} (V)	T_A = 25°C			-40°C ≤ T_A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
I_{OFF}	Power Off Leakage Current	V_I = 5.5V or V_O = 5.5V	0			1		10	uA
I_{CC}	Quiescent Supply Current	V_I = 5.5V or GND	5.5					10	uA

AC Electrical Characteristics

$t_r = t_f$ = 3ns

Symbol	Parameter	Condition	V_{CC} (V)	T_A = 25°C			-40°C ≤ T_A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
t_{pd}	Propagation Delay (Figure3 and 4)	R_L = 1MΩ C_L = 15pF	1.65	2.0	15.5	19.4	2.0	20.9	ns
			1.8	2.0	13.0	17.6	2.0	18.5	
			2.5	0.2	7.5	9.5	0.8	10.2	
		R_L = 1MΩ C_L = 15pF	3.3	0.8	6.0	7.5	0.5	8.0	
		R_L = 500Ω C_L = 50pF		1.2	6.6	8.1	1.5	8.6	
		R_L = 1MΩ C_L = 15pF	5.0	0.5	4.4	5.6	0.5	6.1	
		R_L = 500Ω C_L = 50pF		0.8	4.8	6.1	0.8	6.6	
t_{PZL}, t_{PZH}	Output Enable Time (Figure5 and 6)	R_L = 500Ω C_L = 50pF	1.65	2.0	12.9	20.0	2.0	23.0	ns
			1.8	2.0	10.8	18.0	2.0	22.0	
			2.5	1.5	7.2	8.8	1.5	9.4	
			3.3	1.5	5.8	7.1	1.5	7.6	
			5.0	0.8	4.5	5.5	0.8	5.9	
t_{PLZ}, t_{PHZ}	Output Disable Time (Figure5 and 6)	R_L = 500Ω C_L = 50pF	1.65	2.0	11.5	20.0	2.0	23.0	ns
			1.8	2.0	9.8	18.0	2.0	22.0	
			2.5	1.5	7.7	9.5	1.5	10.1	
			3.3	1.0	7.2	8.8	1.0	9.4	
			5.0	0.5	5.4	6.8	0.5	7.3	

Capacitance Characteristics

Symbol	Parameter	Condition	Typ	Unit
C_{IN}	Input Capacitance	V_{CC} = 5.5V, V_I = 0V or V_{CC}	5	pF
C_{PD}	Power Dissipation Capacitance (5)	10MHz, V_{CC} = 3.3V, V_I = 0V or V_{CC}	15	pF
		10MHz, V_{CC} = 5.5V, V_I = 0V or V_{CC}	15	

Note5: C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

f_o = output frequency in MHz;

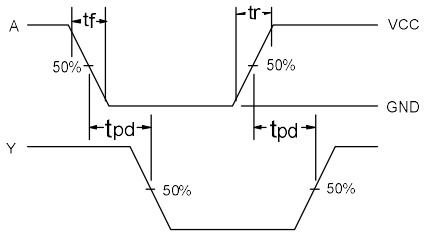
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

$\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

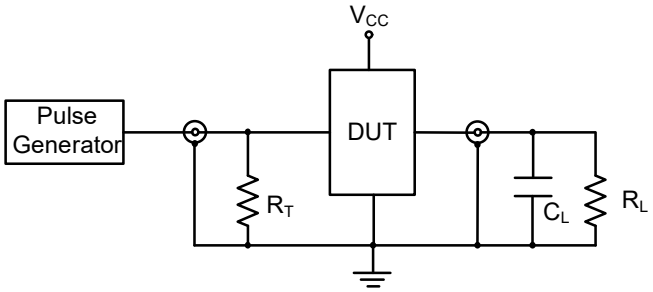
AC Test Circuit



PROPAGATION DELAYS:

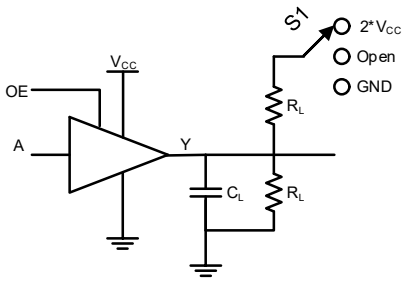
$t_R = t_F$ = 3ns, 10% to 90%; f =1MHz;

Figure3. Switch Waveform



R_T = 50Ω(typ)

Figure4. Test Circuit



Test	Switch
t_{PD}	Open
t_{PZL} t_{PLZ}	2*V_{CC}
t_{PZH} t_{PHZ}	GND

Figure5. Output Enable/Disable Time Test Circuit

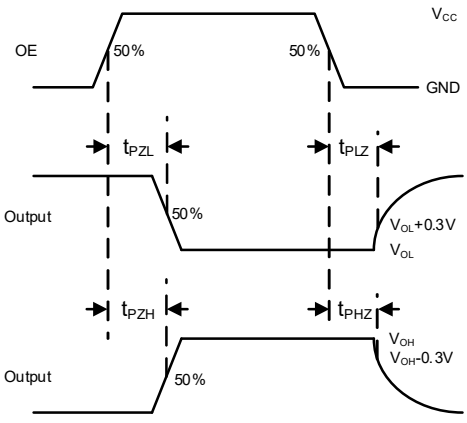
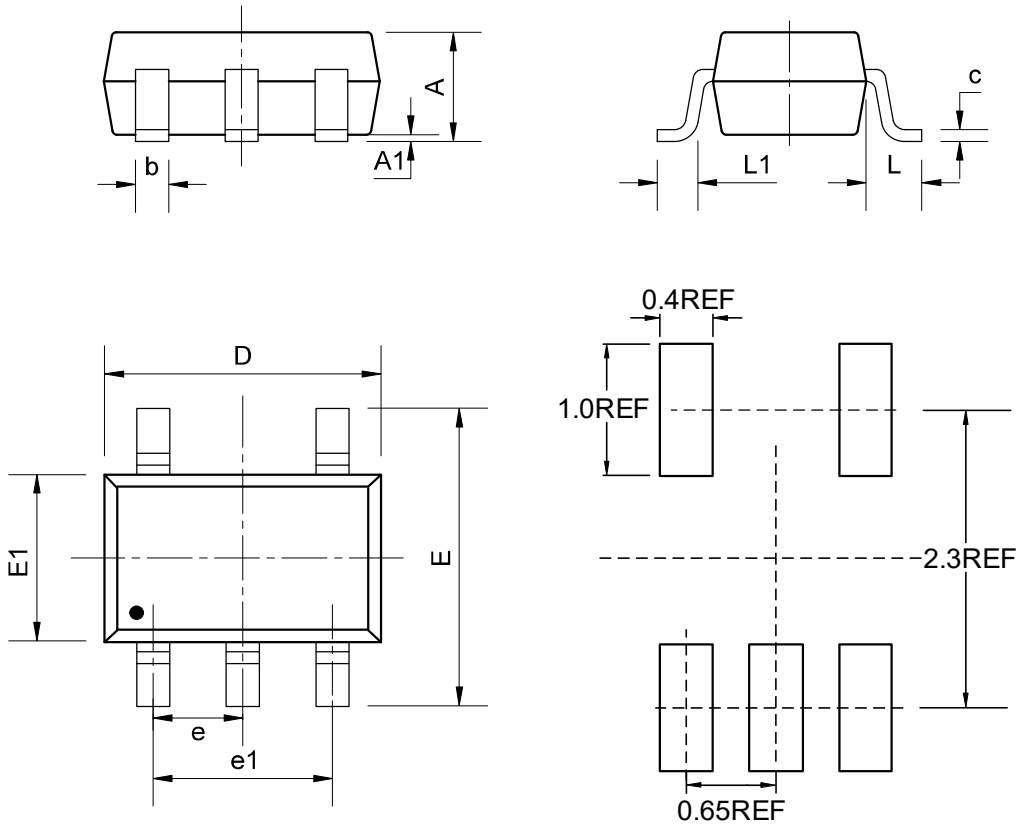


Figure6. Output Enable/Disable Waveform

Package Dimension

SC70-5 (1.3mm × 2.1mm)

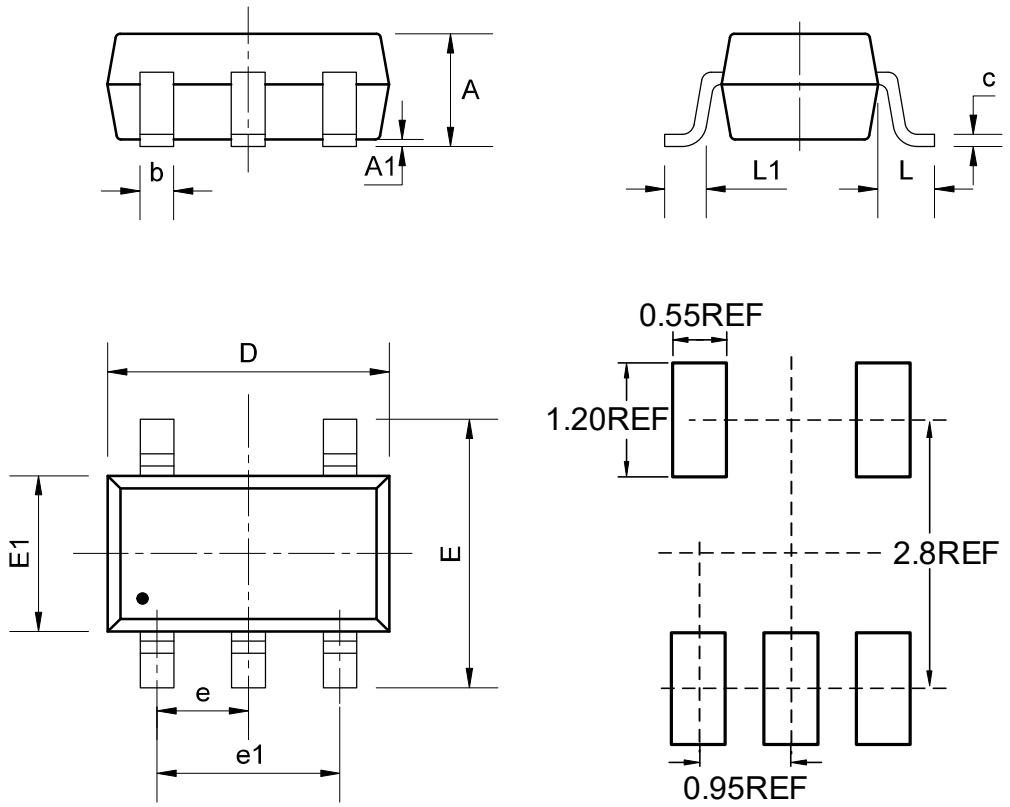


COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.10
A1	0.00	-	0.15
b	0.15	-	0.35
c	0.08	-	0.20
D	2.00	2.10	2.30
e	0.65BSC		
e1	1.30BSC		
E	2.15	2.30	2.50
E1	1.15	1.30	1.45
L	0.50REF		
L1	0.33REF		

SOT23-5 (1.6mm × 2.9mm)

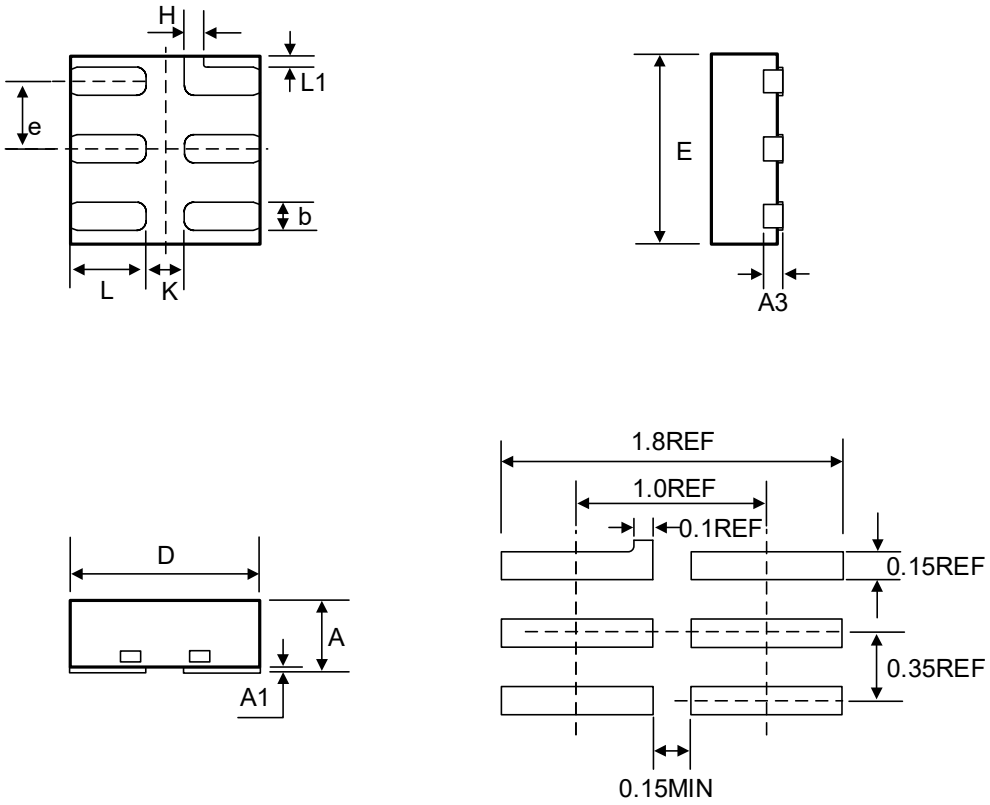


COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.45
A1	0.00	-	0.15
b	0.28	0.35	0.50
c	0.08	0.15	0.22
D	2.75	2.9	3.05
e	0.90	0.95	1.00
e1	1.80	1.90	2.00
E	2.60	2.80	3.00
E1	1.45	1.6	1.75
L	0.60REF		
L1	0.30	0.45	0.60

DFN6 (1.0mm × 1.0mm × 0.37mm)

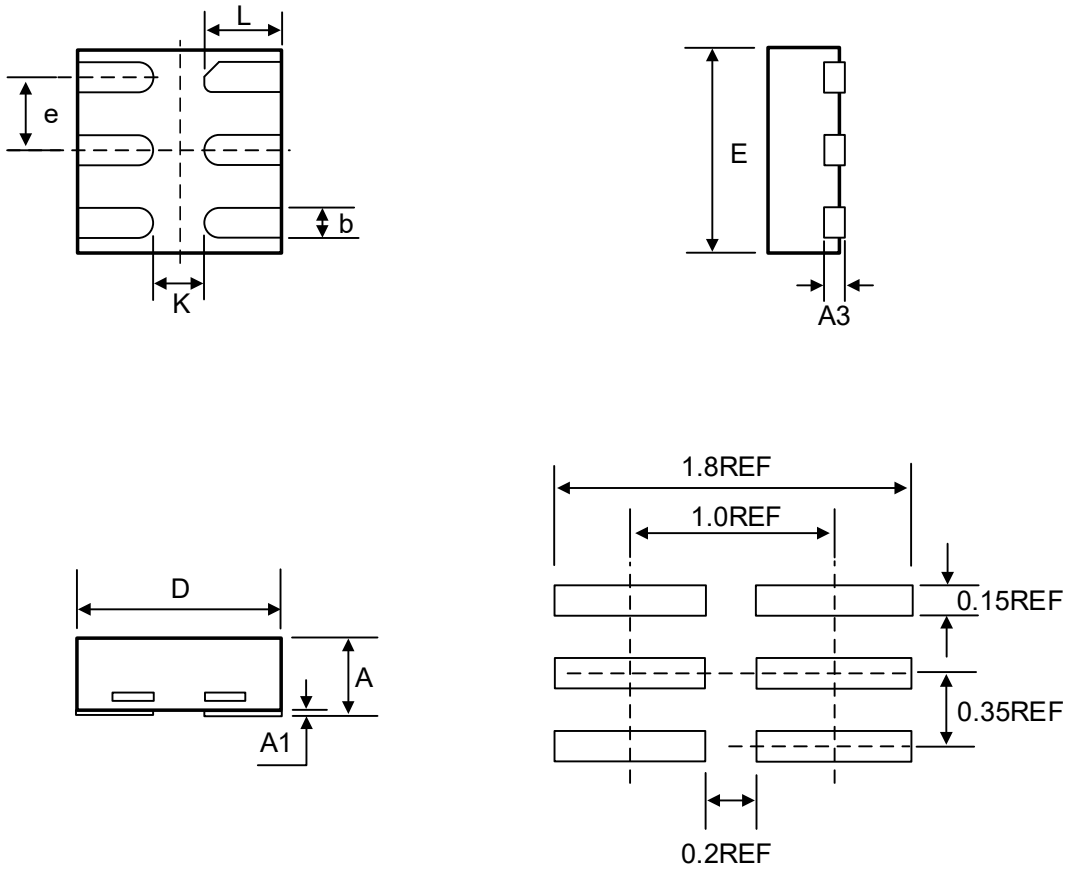


COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	0.34	0.37	0.40
A1	0	0.02	0.05
A3		0.10REF	
b	0.10	0.15	0.20
D	0.95	1.00	1.05
E	0.95	1.00	1.05
e	0.30	0.35	0.40
H		0.10REF	
K	0.15	--	--
L	0.35	0.40	0.45
L1		0.075REF	

DFN6 (1.0mm × 1.0mm × 0.55mm)

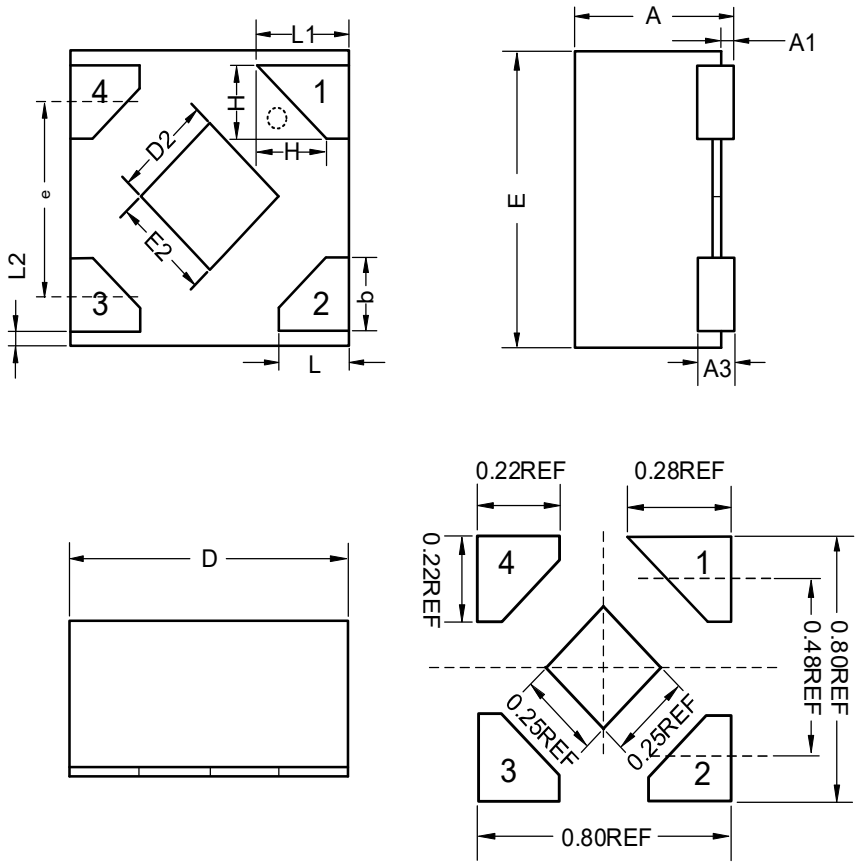


COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	0.50	0.55	0.60
A1	0	-	0.05
A3		0.15REF	
b	0.10	0.15	0.20
D	0.95	1.00	1.05
E	0.95	1.00	1.05
e		0.35BSC	
K		0.20REF	
L	0.30	0.40	0.50

DFN4 (0.8mm × 0.8mm)



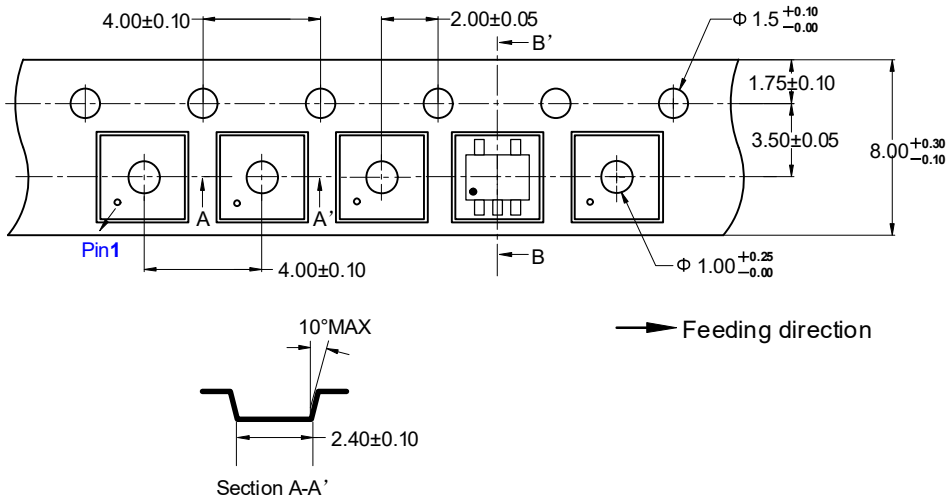
COMMON DIMENSIONS

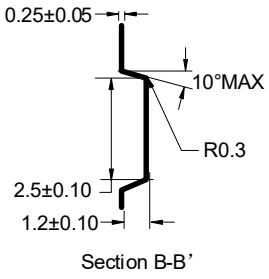
(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	0.34	0.37	0.40
A1	0.00	0.02	0.05
A3		0.10REF	
b	0.17	0.22	0.27
D	0.75	0.80	0.85
E	0.75	0.80	0.85
D2	0.20	0.25	0.30
E2	0.20	0.25	0.30
e	0.43	0.48	0.53
H		0.22REF	
L	0.17	0.22	0.27
L1	0.18	0.28	0.38
L2		0.05REF	

Tape Information

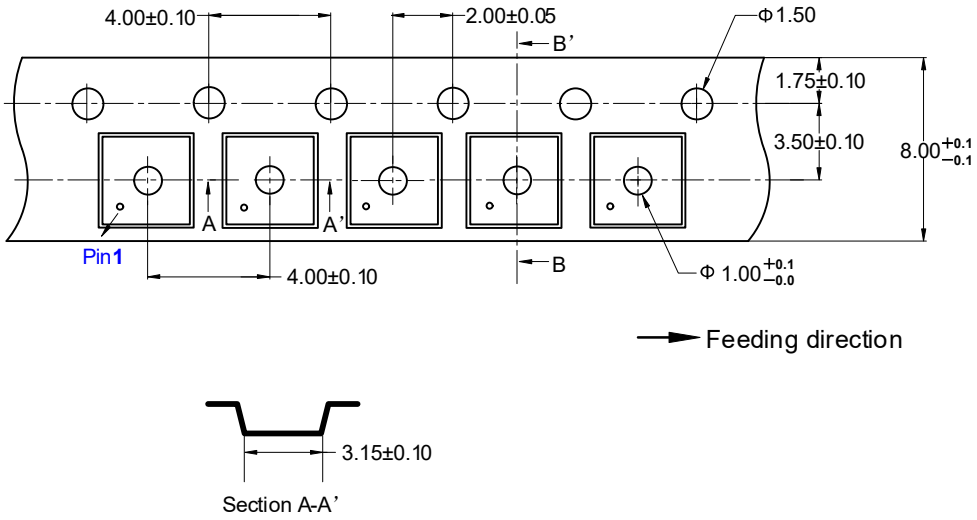
SC70-5 (1.3mm × 2.1mm)

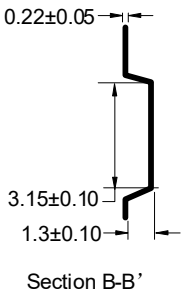




Unit : mm

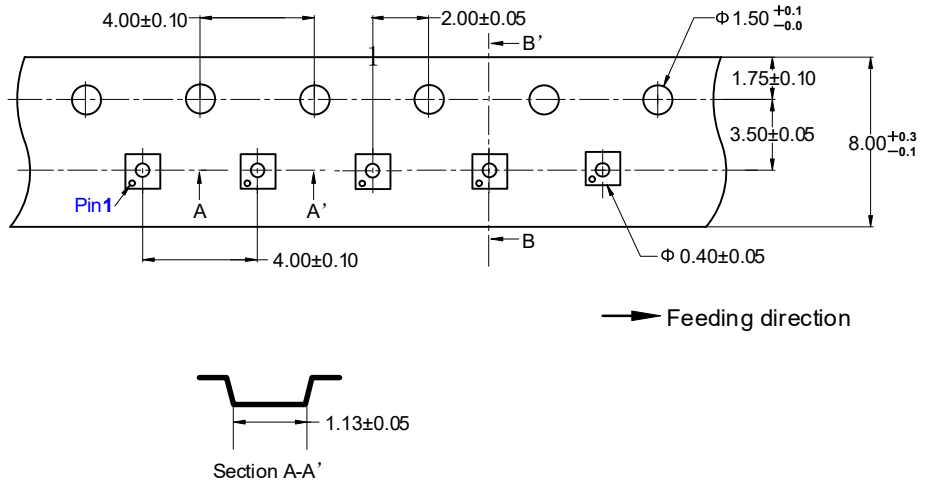
SOT23-5 (1.6mm × 2.9mm)



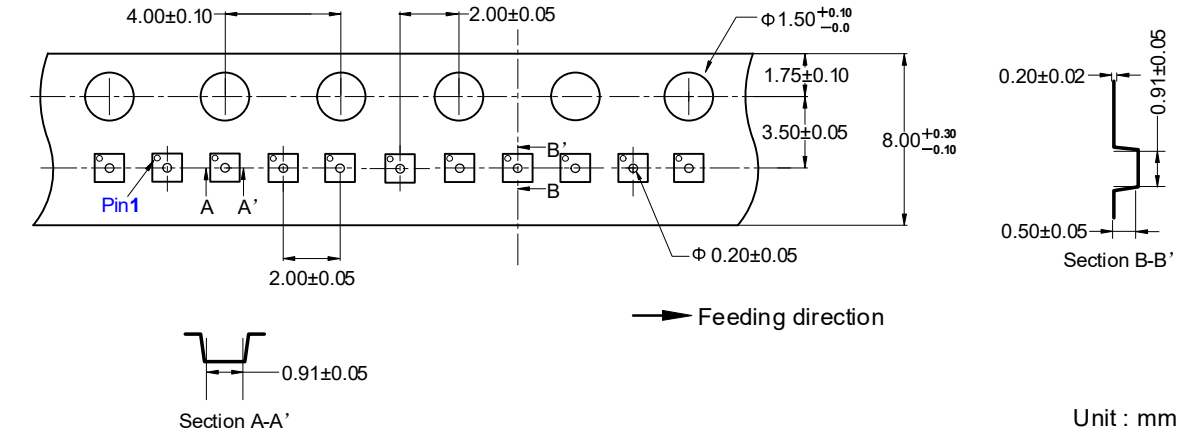


Unit : mm

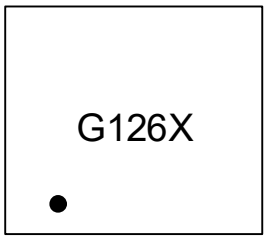
DFN6 (1.0mm × 1.0mm)



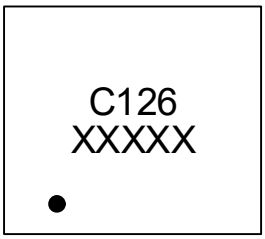
DFN4 (0.8mm × 0.8mm)



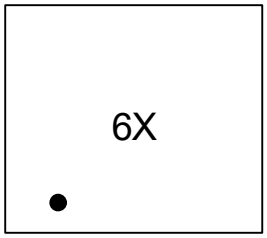
Marking Information



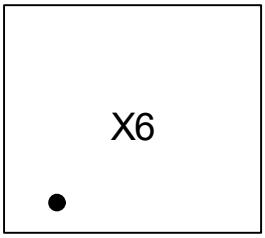
ET74LVC1G126
G126 = Part Number
X = Tracking Number



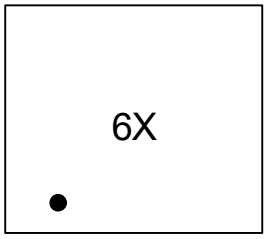
ET74LVC1G126T
C126 = Part Number
XXXXX = Tracking Number



ET74LVC1G126N
6 = Part Number
X = Tracking Number



ET74LVC1G126N1
6 = Part Number
X = Tracking Number



ET74LVC1G126Y1
6 = Part Number
X = Tracking Number

Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2017-10-23	Original Version	Ma yongjian	Ma yongjian	Liu jiaying
1.1	2019-07-18	Update AC Table and Device Information	Ma yongjian	Ma yongjian	Liu jiaying
1.2	2019-08-22	Add Output Enable/Disable Time & Test Circuit	Ma yongjian	Ma yongjian	Liu jiaying
1.3	2023-03-08	ESD Update	Shi bo	Shi bo	Zhu junli
1.4	2024-04-21	Add Marking & Tape	Tu guozhu	Lu hao	Zhu junli
1.5	2025-06-03	Add Packing Option	Yang xiaoxu	Yang xiaoxu	Liu jiaying
1.6	2025-10-30	Add DFN6 Package	Yang xiaoxu	Yang xiaoxu	Liu jiaying
1.6.a	2025-11-06	Update Marking	Yang xiaoxu	Yang xiaoxu	Liu jiaying
1.6.b	2025-12-25	Update Format	Xu tao	Yang xiaoxu	Liu jiaying
1.6.c	2026-03-20	Update Title	Xu tao	Yang xiaoxu	Liu jiaying
1.7	2026-04-20	Add DFN4 Package	Wang anran	Yang xiaoxu	Liu jiaying
1.8	2026-09-09	Update AC Table	Wang anran	Yang xiaoxu	Liu jiaying